

Call for Participation

ASP-DAC 2011

16th Asia and South Pacific Design Automation Conference

Date: January 25-28, 2011

Place: Pacifico Yokohama Conference Center
in Yokohama City, Japan

<http://www.aspdac.com>

Aims of the Conference

ASP-DAC 2011 is the sixteenth in a series of annual international conference on VLSI design automation. Asia and South Pacific region is one of the most active regions of design and fabrication of silicon chips in the world. The conference will provide the CAD/DA and Design community with opportunities of interchanging ideas and collaboratively discussing the directions of the technologies related to all of Electronic Design Automation (EDA).

Features of ASP-DAC 2011

Keynote Speeches

- Takayuki Kawahara, Hitachi Ltd., "Non-Volatile Memory and Normally-Off Computing"
- Ajoy Bose, Atrenta Inc., "Managing Increasing Complexity through High-level of Abstraction: What the past has taught us about the future"
- Subahasish Mitra, Stanford Univ., "Robust System: From clouds to nanotubes"

Designers' Forum

Designers' Forum is a unique program which consists of invited talks and panels to share design experience and solutions of real product designs of the industries. Its topics include chip-package-board co-design and co-verification technologies, advanced packaging and 3D technologies, emerging technologies for wellness applications, and state-of-the-art SoCs.

Tutorials

On the first day, two full-day and four half-day tutorials are scheduled, which will provide the audience with an introduction to hot topics in advanced CMOS device technologies, 3D integration, post-silicon debug, and MPSoC: multiprocessor system on chip.

Technical Sessions

104 papers were selected from 300 submissions.

University LSI Design Contest

IEICE VLD Student Forum at ASP-DAC 2011

Sponsors

ACM SIGDA, IEEE CASS, IEICE ESS, IPSJ SIG-SLDM
Technical Co-Sponsor: IEEE CEDA

Conference Secretariat

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ASIA SOUTH PACIFIC
DAC DESIGN
AUTOMATION
CONFERENCE

Exhibition

Electronic Design and Solution Fair 2011
with FPGA/PLD Design Conference

Date: January 27-28, 2011

Place: Pacifico Yokohama, Yokohama, Japan