

# A Tail-Current Modulated VCO with Adaptive-Bias Scheme

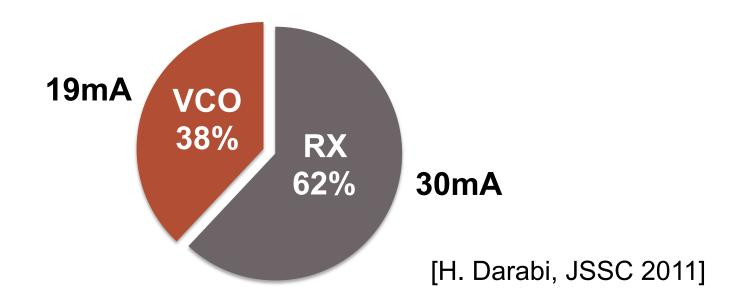
Aravind Tharayil Narayanan, Wei Deng, Kenichi Okada, and Akira Matsuzawa

Tokyo Institute of Technology, Japan



#### **Motivation**

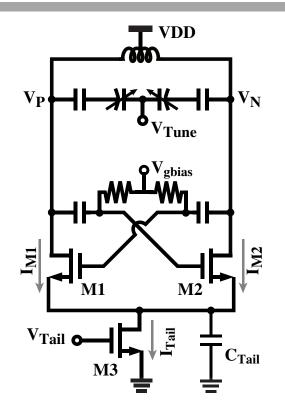
VCO is one of the most power hungry PLL building block.



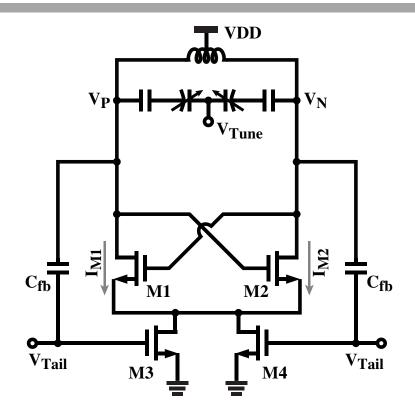
VCO for next generation wireless devices

♦ High purity
♦ High efficiency
♦ Small area

## **High Performance VCOs**



Class-C

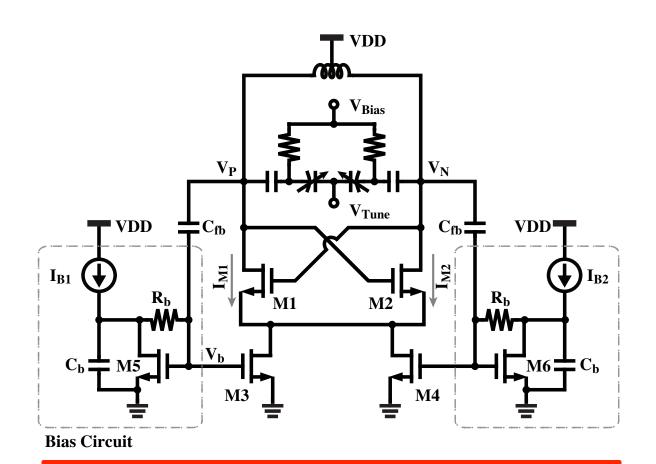


#### Tail-Feedback

[S. Hara, IEICE 2010]

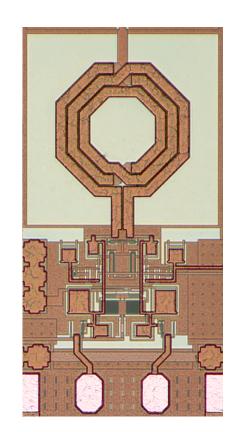
- Better tuning range and noise performance
- Require low V<sub>Tail</sub> for better performance
- VCO fails to start-up at low V<sub>Tail</sub>

### Proposed Adaptive-Bias VCO



Proposed adaptive-bias scheme:

- solves start-up issues
- achieves high-efficiency



CMOS 180nm 530um x 245um

4.6GHz, 6.8mW, -119dBc/Hz@1MHz 4