



Call for Designs

University LSI Design Contest

ASP-DAC 2020

<http://www.aspdac.com/>
January 13-16, 2020
China National Convention Center (CNCC)
Beijing, China

Aims of the Contest:

The University LSI Design Contest will be held as a unique program at ASP-DAC. The purpose of the contest is to encourage education and research on VLSI design at universities and other educational organizations by providing opportunities to present and discuss the innovative and state-of-the-art design. We solicit designs that fit in one or more of the following categories:

- (1) Designed, and actually implemented on chips in universities or other educational organizations during the last two years;
- (2) Designs that report actual measurements from implementations;
- (3) Innovative design prototypes.

Interesting or excellent designs will be honored by providing the opportunities for presentation at a special session of the conference. The awards will be awarded for some of the outstanding designs presented at the conference.

Areas of Design:

The scope of the contest covers circuit techniques for (1) Analog / RF / Mixed-Signal Circuits, (2) Digital Signal Processor, (3) Microprocessors, (4) Custom Application Specific Circuits / Memories, and methodologies for (a) Full-Custom / Cell-Based LSIs, (b) Gate Arrays, and (c) Field Programmable Devices.

Submission of Design Descriptions:

A camera ready summary, including numbers, tables and references, is required to be prepared within 2 pages. It is strongly recommended to include the measured experimental results and micrographs of the chip in the summary. Please do not submit the same paper as a regular paper. Specification of the submission format will be available at <http://www.aspdac.com>

Deadline for summary: 5 PM AOE (Anywhere on earth), July. 5 (Fri), 2019

Notification of acceptance: Sep. 9 (Mon), 2019

Deadline for camera-ready: 5 PM AOE (Anywhere on earth), Nov. 4 (Mon), 2019

Review:

The submitted design will be reviewed by the Design Contest Committee in a process similar to the technical paper review process. The following criteria will be applied to design choices:

- (1) Reliability of design and implementation, (2) Quality of implementation, (3) Performance of the design, (4) Novelty of application, algorithm and architecture (5) Others.

Interesting or excellent designs will be announced at a special session of the conference.

Presentation:

The author of each selected design will be asked to make a short introduction at the ASP-DAC 2020 special session. A summary of each design will be included in the conference proceedings.

Contact Email: aspdac2020.udc@gmail.com

ASP-DAC 2020 Chairs

General Chair:	Tim Cheng (Hong Kong University of Science and Technology), Huazhong Yang (Tsinghua University)
Technical Program Chair:	Tsung-Yi Ho (National Tsing Hua University)
Technical Program Vice Chairs:	Sheldon Tan (University of California, Riverside), Yiran Chen (Duke University)
Design Contest Co-Chairs:	Xiaoyang Zeng (Fudan University), Shouyi Yin (Tsinghua University)