Time table for interactive virtual tutorial sessions

Time zone: JST (UTC+0900)

1/18/21	Room 1		Room 2		Room 3	
	9:00 - 12:00	Tutorial-1 Achieving quantum computing's disruptive capabilities through error-mitigating software	**9:00 - 12:00**	Tutorial-2 Reliability and availability of hardware-software systems — Stochastic Reliability Models of Real Systems	**9:00 - 12:00**	Tutorial-3 Machine Learning in EDA Tutorial: Approaches, Advantages, Challenges and Examples
	Room 1		Room 2			
	14:00 - 17:00	Tutorial-4 The latest Heterogenous Integration Packaging trends for 5G, Artificial Intelligence, Automotive Electronics, and High Performance Computing	**14:00 - 17:00**	Tutorial-5 Emerging Devices from Manufacturing Point of View: 3D NAND Flash Memory, PCRAM and Carbon Nanotube		

Time table for videos broadcasted via Zoom

1/19/21	**8:20**	**1K (Room K)** Opening and Keynote Session I (1K-1/1K-2/1K-3)								
	Room A		Room B		Room C		Room D		Room E	
	10:00	1A University Design Contest I	**10:00**	1B Accelerating Design and Simulation	**10:00**	1C Process-in-Memory for Efficient and Robust AI	**10:00**	1D Validation and Verification	**10:00**	1E Design Automation Methods for Various Microfluidic Platforms
	10:00	1A-1	10:00	1B-1	10:00	1C-1	10:00	1D-1	10:00	1E-1
	10:20	1A-2	10:20	1B-2	10:20	1C-2	10:20	1D-2	10:20	1E-2
	10:40	1A-3	10:40	1B-3	10:40	1C-3	10:40	1D-3	10:40	1E-3
	11:00	1A-4								
	11:20	1A-5								
	11:40	1A-6								
	12:00	1A-7								
	12:20	1A-8								
	11:30	2A University Design Contest II	**11:30**	2B Emerging Non-Volatile Processing-In-Memory for Next Generation Computing	**11:30**	2C (SS-1) Emerging Trends for Cross-Layer Co-Design: From Device, Circuit, to Architecture, Application	**11:30**	2D Machine Learning Techniques for EDA in Analog/Mixed-Signal ICs	**11:30**	2E Innovating Ideas in VLSI Routing Optimization
	11:30	2A-1	11:30	2B-1	11:30	2C-1	11:30	2D-1	11:30	2E-1
	11:50	2A-2	11:50	2B-2	11:50	2C-2	11:50	2D-2	11:50	2E-2
	12:10	2A-3	12:10	2B-3	12:10	2C-3	12:10	2D-3	12:10	2E-3
	12:30	2A-4	12:30	2B-4	12:30	2C-4	12:30	2D-4	12:30	2E-4
	12:50	2A-5								
	13:10	2A-6								
	13:30	2A-7								
	13:50	2A-8								
	13:00	3A (SS-2) ML-Driven Approximate Computing	**13:00**	3B Architecture-Level Exploration	**13:00**	3C Core Circuits for AI Accelerators	**13:00**	3D Stochastic and Approximate Computing	**13:00**	3E Timing Analysis and Timing-Aware Design
	13:00	3A-1	13:00	3B-1	13:00	3C-1	13:00	3D-1	13:00	3E-1
	13:20	3A-2	13:20	3B-2	13:20	3C-2	13:20	3D-2	13:20	3E-2
	13:40	3A-3	13:40	3B-3	13:40	3C-3	13:40	3D-3	13:40	3E-3
	14:00	3A-4	14:00	3B-4	14:00	3C-4	14:00	3D-4	14:00	3E-4

1/20/21	**9:00**	**2K (Room K)** Keynote Session II (2K-1/2K-2)								
	Room A		Room B		Room C		Room D		Room E	
	10:00	4A (SS-3) Technological Advancements inside the AI chips, and using the AI Chips	**10:00**	4B System-Level Modeling, Simulation, and Exploration	**10:00**	4C Neural Network Optimizations for Compact AI Inference	**10:00**	4D Brain-Inspired Computing	**10:00**	4E Cross-Layer Hardware Security
	10:00	4A-1	10:00	4B-1	10:00	4C-1	10:00	4D-1	10:00	4E-1
	10:20	4A-2	10:20	4B-2	10:20	4C-2	10:20	4D-2	10:20	4E-2
	10:40	4A-3	10:40	4B-3	10:40	4C-3	10:40	4D-3	10:40	4E-3
			11:00	4B-4	11:00	4C-4	11:00	4D-4	11:00	4E-4
	11:30	5A (DF-1): New-Principle Computer	**11:30**	5B Embedded Operating Systems and Information Retrieval	**11:30**	5C (SS-4) Security Issues in AI and Their Impacts on Hardware Security	**11:30**	5D Advances in Logic and High-level Synthesis	**11:30**	5E Hardware-Oriented Threats and Solutions in Neural Networks
	11:30	5A-1	11:30	5B-1	11:30	5C-1	11:30	5D-1	11:30	5E-1
	11:50	5A-2	11:50	5B-2	11:50	5C-2	11:50	5D-2	11:50	5E-2
	12:10	5A-3	12:10	5B-3	12:10	5C-3	12:10	5D-3	12:10	5E-3
	12:30	5A-4			12:30	5C-4				
	13:00	6A (DF-2): Advanced Sensing Technology and Automotive Application	**13:00**	6B Advanced Optimizations for Embedded Systems	**13:00**	6C Design and Learning of Logic Circuits and Systems	**13:00**	6D Hardware Locking and Obfuscation	**13:00**	6E Efficient Solutions for Emerging Technologies
	13:00	6A-1	13:00	6B-1	13:00	6C-1	13:00	6D-1	13:00	6E-1
	13:20	6A-2	13:20	6B-2	13:20	6C-2	13:20	6D-2	13:20	6E-2
	13:40	6A-3	13:40	6B-3	13:40	6C-3	13:40	6D-3	13:40	6E-3
	14:00	6A-4								

1/21/21	**9:00**	**3K (Room S)** Keynote Session III (3K-1/3K-2)								
	Room A		Room B		Room C		Room D		Room E	
	10:00	7A (SS-5) Platform-Specific Neural Network Acceleration	**10:00**	7B Toward Energy-Efficient Embedded Systems	**10:00**	7C Software and System Support for Nonvolatile Memory	**10:00**	7D Learning-Driven VLSI Layout Automation Techniques	**10:00**	7E DNN-Based Physical Analysis and DNN Accelerator Design
	10:00	7A-1	10:00	7B-1	10:00	7C-1	10:00	7D-1	10:00	7E-1
	10:20	7A-2	10:20	7B-2	10:20	7C-2	10:20	7D-2	10:20	7E-2
	10:40	7A-3	10:40	7B-3	10:40	7C-3	10:40	7D-3	10:40	7E-3
	11:00	7A-4	11:00	7B-4	11:00	7C-4	11:00	7D-4	11:00	7E-4
	11:20	7A-5								
	11:30	8A (DF-3): Emerging Open Design Platform	**11:30**	8B Embedded Neural Networks and File Systems	**11:30**	8C (SS-6) Design Automation for Future Autonomy	**11:30**	8D Emerging Hardware Verification	**11:30**	8E Optimization and Mapping Methods for Quantum Technologies
	11:30	8A-1	11:30	8B-1	11:30	8C-1	11:30	8D-1	11:30	8E-1
	11:50	8A-2	11:50	8B-2	11:50	8C-2	11:50	8D-2	11:50	8E-2
	12:10	8A-3	12:10	8B-3	12:10	8C-3	12:10	8D-3	12:10	8E-3
					12:30	8C-4				
	13:00	9A (DF-4): Technological Utilization in COVID-19 Pandemic	**13:00**	9B Emerging System Architectures for Edge-AI	**13:00**	9C (SS-7) Cutting-Edge EDA Techniques for Advanced Process Technologies	**13:00**	9D (SS-8) Robust and Reliable Memory Centric Computing at Post-Moore	**13:00**	9E Design for Manufacturing and Soft Error Tolerance
	13:00	9A-1	13:00	9B-1	13:00	9C-1	13:00	9D-1	13:00	9E-1
	13:20	9A-2	13:20	9B-2	13:20	9C-2	13:20	9D-2	13:20	9E-2
	13:40	9A-3	13:40	9B-3	13:40	9C-3	13:40	9D-3	13:40	9E-3
	14:00	9A-4	14:00	9B-4			14:00	9D-4	14:00	9E-4
									14:20	9E-5