

REGISTRATION FORM ASP -DAC2004

January 27 - 30, 2004 Yokohama, Japan

ASP - DAC 2004 SECRETARIAT
Japan Electronics Show Association
Sumitomo Shibadaimon Bldg. 2gokan,5F
1-12-16 Shibadaimon
Minato-ku, Tokyo, 105 - 0012 JAPAN

Miliatorku, Tokyo, 103-0012 JAPAN

Tel: +81 -3-5402-7601 Fax: +81-3-5402-7605

E-mail: aspdac2004@aspdac.com

All items marked by * are required fields.

Text fields must be typed by single byte character.

Using this form on Internet Explorer 5.x is not recommended, as it may cause some problems.

Please use other browsers. If you have to use 5.x for some reason, turning off the Language Auto-sensing might somewhat improve the situation.

* OProf. ODr.	● Mr. OMs.	(敬称を選択)
* Family Name	Koizumi	(姓)
* First Name	Taro	(名)
Other Name		(洗礼名など)
* Affiliation	JESA University	(所属する会社名や大学名)
Mailstop	Design Automation I	_aboratory (社内や学内での郵便配達先)
Dept./Div.	Dept. of Electronic	c Engineering (所属する部局課名)
* Mailing Address	1-12-16 Shibadaimor	n, Minato-ku (所属先の住所)
* City	Tokyo	(所属先の都市名)
State		(所属先の州名)
* Postal Code	105-0012	(所属先の郵便番号)
* Country	Japan	(所属先の国名・下のAreaでJapanを選択)
* Area	● Japan O Asia South	n Pacific O North America O Europe/Africa O Other
* Phone	+81-3-5402-7601	(所属先の電話番号・国別コードから始める)
* Fax	+81-3-5402-7605	(所属先のFAX番号・国別コードから始める)
* E-mail	aspdac2004@aspdac.	com(電子メイルアドレス)
* Membership	O No Membership	
•		ACM SIGDA O IEEE(関連学会の会員か否か・該当学会を選択)
Member Code	123456	
* Are you a speak	er! session No.(s) is/are	
O Tes, tile		
● No.	, , , , , , , , , , , , , , , , , , , ,	

* Member Status: O Member O Non-member ● Full-time student O Press (関連学会会員/学会非会員/(会社から給料をもらっていない)学生/報道)から選択

- * Tutorial (どのTutorialに参加するか、どれにも参加しないかを選択)
 - Tutorial 1 (Full -Day Tutorial): "Recent Advances and Future Challenges in Design Verification"
 - O Tutorial 2 (Full -Day Tutorial): "Design and Runtime Techniques for Leakage Control and Minimization of CMOS VLSI Circuits in Active and Sleep Modes"
 - O Tutorial 3 (Full -Day Tutorial): "System -Level Design Methodology for SoC Design"
 - O Tutorial 4.1 (Half -Day Tutorial): "Low Power Design Techniques and Tools"
 - O Tutorial 4.2 (Half -Day Tutorial): "Energy, Fault -Tolerance, and Scalability Issues in Designing Networks-on-chip"
 - O Tutorial 4.1 and 4.2 (Two Half -Day Tutorials): "Low Power Design Techniques and Tools" and "Energy, Fault-Tolerance, and Scalability Issues in Designing Networks-on-chip"
 - O No Tutorial (Conference Only)
- * Schedule (どの日程で会議に参加するかを、全てに参加、1日だけ参加(日を指定する)、
 - Full attendance

会議には不参加から選択)

- One-Day ONLY (Please be sure to tick the date you will attend.)
 O Jan. 28 O Jan. 29 O Jan. 30
- No Conference (Tutorial Only)
- * Registration Fee remitted

Conference	by Dec. 19, '03	after Dec. 20, '03 and on-site	
Member	*38,000 yen	*42,000 yen	
Non-member	*48,000 yen	*52,000 yen	
Student	18,000 yen	22,000 yen	
One-Day ONLY	25,000 yen	27,000 yen	

^{*}indicates that a banquet ticket is included in the fee.

You may purchase a banquet ticket at 5,000 yen per person on site.

Full -Day Tutorial and two Half -Day Tutorials	by Dec. 19, '03	after Dec. 20, '03 and on-site
Member	24,000 yen	28,000 yen
Non-member	30,000 yen	34,000 yen
Student	15,000 yen	17,000 yen
Half -Day Tutorial	by Dec. 19, '03	after Dec. 20, '03 and on-site
Member	15,000 yen	17,000 yen
Non-member	18,000 yen	20,000 yen
Student	9,000 yen	10,000 yen

If you are using Netscape Navigator 3.0 or newer, the fields below will show the costs based on your form selections and today's date. If you are using an older version of Netscape Navigator, or another browser, the costs can be derived from the table above, and will be displayed on the confirmation page.

Please do NOT edit these fields. The calculated values are what you will be charged.

Conference Fee	0	
Conference ree		yer
Tutorial Fee	0	

		yen
TOTAL COST	0	
		yen

- * Payment Method (choose one only) (参加費の支払い方法を選択
 - BANK TRANSFER 日本在住の場合にはクレジットカード不可・銀行振込のみ)
 - O CREDIT CARD (Credit cards will be accepted only for residents outside Japan.)

BANK TRANSFER (金額は自動計算されます)

If you select bank transfer, all items required fields.

I remitted or will remit a grand total of (自動計算) yen on 19/12/2003 (date/month/year) through my bank Bank of Tokyo-Mitsubishi (bank name) (振り込んだ日もしくは予定日) to the following accout: (振り込み元の銀行名)

振込先

三井住友銀行 丸ノ内支店 普通口座

口座番号: 6583544

口座名義: ASP-DAC2004 代表 今井 正治

*インターネット振込の場合の振込先名義は下記になります。

エーエスピーダック2004 ダイヒョウ イマイ マサハル

(上記名義を間違いなくご入力頂きませんと振込が完了しません。)

CREDIT CARD

If you select credit card, all items required fields.

Your Credit Card	O Visa	O MasterCard	O American Express	O Diners Club
Amount to be paid		yen		
Card Number		-		
Cardholder's Name				
Expiration Date		(month/y	ear)	

Notes:

- 1. All payments must be in Japanese yen.
- 2. Bank drafts and personal checks will not be accepted.
- 3. If paying by credit card, please send this form by web.
- 4. 振込人名義は参加登録した名前と同じにしてください。
- 5. もしも会社名等で振り込んだ場合には、<u>aspdac2004@aspdac.com</u>宛に電子メイルで、 もしくは 03-5402-7605宛にFAXで、参加登録した人のID番号と振り込んだ日を 1週間以内にご連絡下さい。
- 6. なお、振込手数料は参加登録者の負担といたします。
- 7. If payment of the registration fee is unremitted, or the credit card charge cannot be authorized, please go to the accounting desk.
- 8. 参加登録者の登録情報に変更が生じた場合、ID番号と共に、<u>aspdac2004@aspdac.com</u>宛に 電子メイルで、もしくは 03-5402-7605宛にFAXでご連絡下さい。
- 9. 参加登録した人のID番号は、参加登録を完了後に電子メイルにて通知されます。

* Invoice required?	Yes	O No (請求書が必要か否かを選択)
If you replied "Yes," and you would that of your registration, please inp		a different address from
* Invoice to (name) :	Taro KOIZUMI	(請求書の宛名)
address:	(請求書の送り先が上で 異なる場合にだけ記入	
* Receipt required?	Yes	O No (領収書が必要か否かを選択)
If you replied "Yes," and you would that of your registration, please inp	•	a different address from
* Receipt to (name)	Taro KOIZUMI	(領収書の宛名)
address (領収書の送り先が上で記入した住所と 異なる場合にだけ記入)		
Attendee Survey		
* 1) Which category best describes	your work? (choose one only	り (職種を1つだけ選択)
O Exec./Senior Mgmt.		O CAD Developer
O Design Eng. Mgmt.	O Researcher/Educator	
O Development Eng.	O Marketing/Sales	
O Other Mgmt.		O Student
O Design Engineer		• Other
* 2) What design area(s) are you foc	used on? (何のデザインI	こ興味があるかを選択・複数可)
☑ Digital ICs		☐ ASICs
☑ Digital Systems		□ PLDs
☐ Analog ICs/Mixed si	-	☑ FPGAs
☐ Analog Systems/Mi	xed signal systems	☐ PCB Layout
Remarks (if any) *English Only		
(何か知らせたいことがあった	ら記入)	<u></u>
	Submit Clear	