



## Call for Participation

### ASP-DAC 2006

Asia and South Pacific  
Design Automation Conference 2006

January 24 - 27, 2006

Pacifico Yokohama Conference Center

in Yokohama City, Japan.

<http://www.aspdac.com/>

\*\*\* EARLY REGISTRATION DUE: Dec. 16, 2005 \*\*\*

#### Aims of the Conference:

ASP-DAC 2006 is the eleventh in a series of annual International Conferences on Design Automation. Asia and South Pacific Region is one of the most active regions of design and fabrication of silicon chips in the world. The conference aims at providing the Asian and South Pacific CAD/DA and Design community with opportunities of interchanging ideas and collaboratively discussing the directions of the technologies related to EDA technologies and design methodologies of electronic systems.

#### Features of ASP-DAC 2006:

##### Keynote Speeches:

We have three keynote speakers from academia, the semiconductor industry and the systems industry.

Wednesday, 25 January, "Automotive Electronics: Steady Growth for Years to Come!,"

Professor Alberto Sangiovanni-Vincentelli, University of California at Berkeley,

Thursday, 26 January, "Challenging Device Innovation," Satoru Ito, President & CEO, RENESAS Technology Corp.,

Friday, 27 January, "Effective Platform-based Development for Large-scale Systems Design,"

Yukichi Niwa, Senior Advisory Director, CANON INC.

**The Technical Program** Committee carefully reviewed 424 papers and carefully selected 135 papers. It is an outstanding program that covers a variety of key topics from system level design to physical design. Four Special sessions covers up-to-date topics of nanoscale design, flash memory embedded systems, H264/AVC etc.

**Designers' Forum** is a new program that shares design experience and solutions of real product designs of the industries whose topics include the CELL and mobile designs, panels of top 10 design issues and system verification.

**Tutorials:** Tuesday, 24 January, two full-day and four half-day tutorials are scheduled to provide introductions to hot topics like DFM, lowpower, packaging/interconnect, system level design and verification.

**The University LSI Design Contest** is also an important event of ASP-DAC, which focuses a real chip design in academia. The Design Contest Committee selected 19 designs for presentation, from which winners of design awards will be announced in the conference.

**Ph.D. Forum** provides a great opportunity for Ph.D. students to get feedback and have discussion with people from academia and industry

For detailed information, visit our WEB site: <http://www.aspdac.com/>

#### Sponsors:

IEEE CAS, ACM SIGDA, IEICE ESS (Institute of Electronics, Information and Communication Engineers - Engineering Sciences Society), IPSJ SIGSLDM (Information Processing Society of Japan - SIG System LSI Design Methodology).

#### ASP-DAC 2006 Chairs:

General Chair:

Fumiyasu Hirose (Cadence Design Systems, Japan)

Technical Program Chair:

Hidetoshi Onodera (Kyoto University, Japan)

Technical Program Vice Chair

Yusuke Matsunaga (Kyushu University, Japan)

University Design Contest Co-Chairs:

Kazutoshi Kobayashi (Kyoto University, Japan)

Takahiko Arakawa (Renesas Technology Corp., Japan)

Designers' Forum Chair:

Makoto Ikeda (University of Tokyo)

Tutorial Co-Chairs:

Kazutoshi Wakabayashi (NEC Corp., Japan)

Chung-Kuan Cheng (University of California  
San Diego, U. S. A.)

Tutorial Vice Chair:

Hiroyuki Higuchi (Fujitsu Laboratories, Japan)