

# Flow Time Minimization under Energy Constraints

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## Agenda

### Introduction

System Models and Preliminary Results

### Our Algorithms

- Flow Time Minimization
  - Ideal Processors
  - Non-Ideal Processors
- Weighted Flow Time Minimization
- Performance Evaluation
- Conclusion







### Introduction

#### Energy-efficiency is important in system designs







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### Hardware Methodology for Power Saving

- Dynamic power management (DPM)
  The operation mode of the system
  ACPI
- Micro-architecture technique
  - Adaptive architecture
  - Cache management
- Dynamic voltage scaling (DVS)
  - Supply voltage scaling
    - Intel Xscale, StrongARM; Transmeta Crusoe
    - Intel SpeedStep, AMD PowerNow!
  - Threshold voltage scaling



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# Dynamic Voltage Scaling

- A higher supply voltage usually results in a higher frequency (or higher execution speed)
  - s =  $k * (V_{dd} V_t)^2 / (V_{dd})$ , where
    - s is the corresponding speed of the supply voltage  $V_{dd}$  and
    - $V_t$  is the threshold voltage
- The dynamic power consumption function  $P_d()$  of the execution speeds of a processor is a convex function:
  - P<sub>d</sub>(s) =  $C_{ef} V_{dd}^2 s$ , in which  $C_{ef}$  is the switch capacitance related to tasks under executions
  - $P_d(s) = C_{ef} s^3 / k^2 \text{ , when } V_t = 0$
- The static power consumption comes from the leakage current
  - A constant or
  - A linear function of the supply voltage





### Energy-Efficient Scheduling versus Energy-Constrained Scheduling

Energy-efficient scheduling is to minimize the energy consumption while the performance index or the timing constraint is guaranteed

Energy-constrained scheduling is to maximize the performance or system rewards under a specified energy constraint





## Performance Maximization

Reward Maximization Rusu et al. (RTSS'02) Kang et al. (RTSS'02) Rusu et al. (ECRTS'03) Chen et al. (SAC'04) AlEnawy and Aydin (ECRTS'04) Chen and Kuo (RTSS'05) Flow Time Minimization Albers and Fujiwara (STACS'06) Pruhs et al. (SWAT'04) Completion Time Minimization







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### System Models

- Processor model

  - Ideal processors: s<sub>min</sub> ~ s<sub>max</sub>
    Non-ideal processors: (s<sub>min</sub> = s<sub>1</sub>, s<sub>2</sub>, ..., s<sub>M</sub>=s<sub>max</sub>)
  - Power consumption function: P(s)
    - P(s) is a convex and increasing function
    - P(s)/s is an increasing function
- Job model
  - Each job J<sub>j</sub> is associated with its computation requirement in CPU cycles: c<sub>j</sub>
  - The flow time of a job: the interval length during the release time and the completion time of the job
  - Each job arrives at the same time: 0
- Our objective
  - Find a schedule for a given job set  $J = (J_1, J_2, ..., J_N)$  such that the energy consumption is no more than the energy constraint  $E_{h}$ , and the average flow time of these N jobs is minimized







### **Execution Behavior for Optimal Solutions**

There exists an optimal schedule which executes jobs in J in a non-decreasing order of their CPU execution cycles for both ideal and non-ideal processors





We index jobs so that the execution cycles of the jobs in J are in a non-decreasing order, i.e., shorted job first



## A Motivational Example

 $c_1 = 5$  $c_2 = 6$  $c_3 = 9$ 

speed just meet the energy constraint  $C_3 = 9$   $P(s) = s^3$ 0 10 22 40 time  $E_b = 5$ 

Execute jobs at the speed that

Average flow time = (10+22+40)/3 = 24







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# Scheduling for Ideal Processors

An optimal solution executes job J<sub>i</sub> at a speed: r<sub>i</sub>

The energy consumption is

$$\sum_{j=1}^{N} P(r_j) \frac{c_j}{r_j}$$

Executing jobs from J<sub>1</sub> to J<sub>N</sub> consecutively leads to a solution

The flow time of job  $J_j$  is  $\sum_{i=1}^{j} c_i / r_i$ 

The average flow time is  $\sum_{j=1}^{N} \sum_{i=1}^{j} \frac{c_i}{r_i} = \sum_{j=1}^{N} (N-j+1) \frac{c_j}{r_j}$ .

There exists an optimal schedule with  $r_1 \ge r_2 \ge ... \ge r_N$ 



### Algorithm LM: Optimal Solutions





# Algorithm Greedy for Non-Ideal Processors







# Weighted Flow-Time Minimization

If the execution order is determined

Apply simple revisions of Algorithm LM or Algorithm Greedy to determine the execution speeds

The weighted flow time is

$$\sum_{j=1}^{N} \left(\sum_{i=j}^{N} w_i\right) \frac{c_j}{r_j}$$

Otherwise,

Applying the well-known weighted shortest-job-first strategy as the execution order, i.e., c<sub>i</sub>/w<sub>i</sub> in an increasing order







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## **Evaluation Setup**

#### Processor:

Intel XScale: 150, 400, 600, 800, 1000 MHz with 80, 170, 400, 900, 1600 mWatt

• Normalized so that the highest speed is 1

Ideal processor approximation:  $s_{min} = 0.15$ ,  $s_{max} = 1 \text{ with } P(s) = 0.08 + 1.52 \text{ s}^{3}$ 

Jobs:

Execution cycle is a random variable in (0, 1]
 The weight of a job is a random variable in (0.1, 10.1]



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### Evaluation Results(1)





Emax (Emin, respectively) is the energy consumption by executing all the jobs at speed  $s_{max}$  ( $s_{min}$ , respectively). Energy consumption constraint  $E_{b}$  is set as Emin +  $\gamma$  (Emax – Emin).

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### Evaluation Results(2)



 $\gamma$  is 0.4



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## Conclusion and Future Work

#### Conclusion

#### Optimal scheduling algorithms

• Flow time minimization under energy constraints in ideal and non-ideal processors

#### Heuristic scheduling algorithms

• Weight flow-time minimization under energy constraints

#### Future work

- Minimization of the average flow time for jobs with different arrival times
- Worst-case analysis for the minimization of the average weighted flow time under a given energy constraint





### Questions and Suggestions?



