Call for Participation
ASP-DAC 2007

Asia and South Pacific
Design Automation Conference 2007
January 23 - 26, 2007
Pacifico Yokohama Conference Center
in Yokohama City, Japan
http://www.aspdac.com/
*** EARLY REGISTRATION DUE: Dec. 15, 2006 ***

Aims of the Conference:

ASP-DAC 2007 is the twelfth in a series of annual International Conferences on Design Automation. Asia and South Pacific Region is one of the most active regions of design and fabrication of silicon chips in the world. The conference aims at providing the Asian and South Pacific CAD/DA and Design community with opportunities of interchanging ideas and collaboratively discussing the directions of the technologies related to EDA technologies and design methodologies of electronic systems.

Features of ASP-DAC 2007:

Keynote Speeches:

We have three keynote speakers from academia and industry.

Wednesday, 24 January, "**Next-Generation Design and EDA Challenges: Small Physics, Big Systems, and Tall Tool-chains**," Professor Rob A. Rutenbar, Electrical & Computer Engineering, Carnegie Mellon University, and Director, C2S2

Thursday, 25 January, "**Meeting with the Forthcoming IC Design - The Era of Power, Variability and NRE Explosion and a Bit of the Future -**," Professor Takayasu Sakurai, Center for Collaborative Research, and Institute of Industrial Science, University of Tokyo

Friday, 26 January, "**How Foundry can Help Improve your Bottom-Line? Accuracy Matters!**," Dr. Fu-Chieh Hsu, Vice President of Design and Technology Platform, Taiwan Semiconductor Manufacturing Company

Technical Program Committee carefully reviewed 408 papers and carefully selected 131 papers. It is an outstanding program that covers a variety of key topics from system level design to physical design. Four Special sessions cover up-to-date topics of DFM, embedded software, EDA challenges, and multi-processor platforms.

Designers' Forum is a new program which started in the last ASP-DAC. We share design experience and solutions of real product designs of the industries. Its topics include low-power SOC technologies, high-speed chip to chip signaling solutions, presilicon SoC HW/SW verification, and top 10 design issues.

Tutorials: Tuesday , 23 January, two full-day and four half-day tutorials are scheduled to provide introductions to hot topics like DFM tools and methodologies, functional verification, low power, physical design, and practical embedded system design.

The University LSI Design Contest is also an important event of ASP-DAC, which focuses on real chip designs in academia. The Design Contest Committee selected 18 designs for presentation, from which winners of design awards will be announced in the conference.

Student Forum provides a great opportunity for Ph. D. and graduate students to get feedback and have discussion with people from academia and industry

For detailed information, visit our WEB site: http://www.aspdac.com/

Sponsors:

IEEE Circuits and Systems Society, ACM SIGDA, IEICE ESS*, and IPSJ SIGSLDM**.
*Institute of Electronics, Information and Communication Engineers - Engineering Sciences Society
** Information Processing Society of Japan - SIG System LSI Design Methodology

ASP-DAC 2007 Chairs:

General Chair:
  Hidetoshi Onodera (Kyoto University, Japan)
Technical Program Chair:
  Yusuke Matsunaga (Kyushu University, Japan)
Vice Technical Program Co-chairs
  Kiyoung Choi (Seoul National University, Korea)
  Youn-Long Lin (National Tsing Hua University, Taiwan)
Design Contest Chair:
  Makoto Nagata (Kobe University, Japan)

Designers Forum Chair:
  Haruyuki Tago (Toshiba, Japan)
Designers Forum Vice Chair
  Kazutoshi Kobayashi (Kyoto University, Japan)
Tutorial Chair:
  Makoto Ikeda (University of Tokyo, Japan)