

Call for Participation

ASP-DAC 2009

14th Asia and South Pacific Design Automation Conference

Date: January 19-22, 2009

Place: Pacifico Yokohama Conference Center
in Yokohama City, Japan

<http://www.aspdac.com>

Aims of the Conference

ASP-DAC 2009 is the fourteenth in a series of annual international conference on VLSI design automation. Asia and South Pacific region is one of the most active regions of design and fabrication of silicon chips in the world. The conference will provide the CAD/DA and Design community with opportunities of interchanging ideas and collaboratively discussing the directions of the technologies related to all of Electronic Design Automation (EDA).

Features of ASP-DAC 2009

Keynote Speeches

- Mitsuo Saito, Toshiba Corp., "Challenges to EDA System from the View Point of Processor Design and Technology Drivers"
- Wolfgang Rosenstiel, University of Tuebingen, "Automated Synthesis and Verification of Embedded Systems: Wishful Thinking or Reality?"
- Leon Stok, IBM Corp., "From Restrictive to Prescriptive Design"

Designers' Forum

Designers' Forum is a unique program which consists of invited talks and panels to share design experience and solutions of real product designs of the industries. Its topics include up-to-date consumer SoCs, analog technologies, dynamic reconfigurable processors, and upstream design methods.

Tutorials

On the first day, one full-day and six half-day tutorials are scheduled, which will provide the audience with an introduction to hot topics in design for variability, reliable circuit design, low leakage design, multi-core software development, embedded system design, and functional verification.

Technical Sessions

116 papers were selected from 355 submissions.

University LSI Design Contest

IEICE VLD Student Forum at ASP-DAC 2009

Sponsors

ACM SIGDA, IEEE CASS, IEICE ESS, IPSJ SIG-SLDM
Technical Co-Sponsor: IEEE CEDA

Conference Secretariat

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ASIA SOUTH PACIFIC
DAC DESIGN
AUTOMATION
CONFERENCE

Exhibition

Electronic Design and Solution Fair 2009
with FPGA/PLD Design Conference

Date: January 22-23, 2009

Place: Pacifico Yokohama, Yokohama, Japan