

Scan-Based Attack against Elliptic Curve Cryptosystems

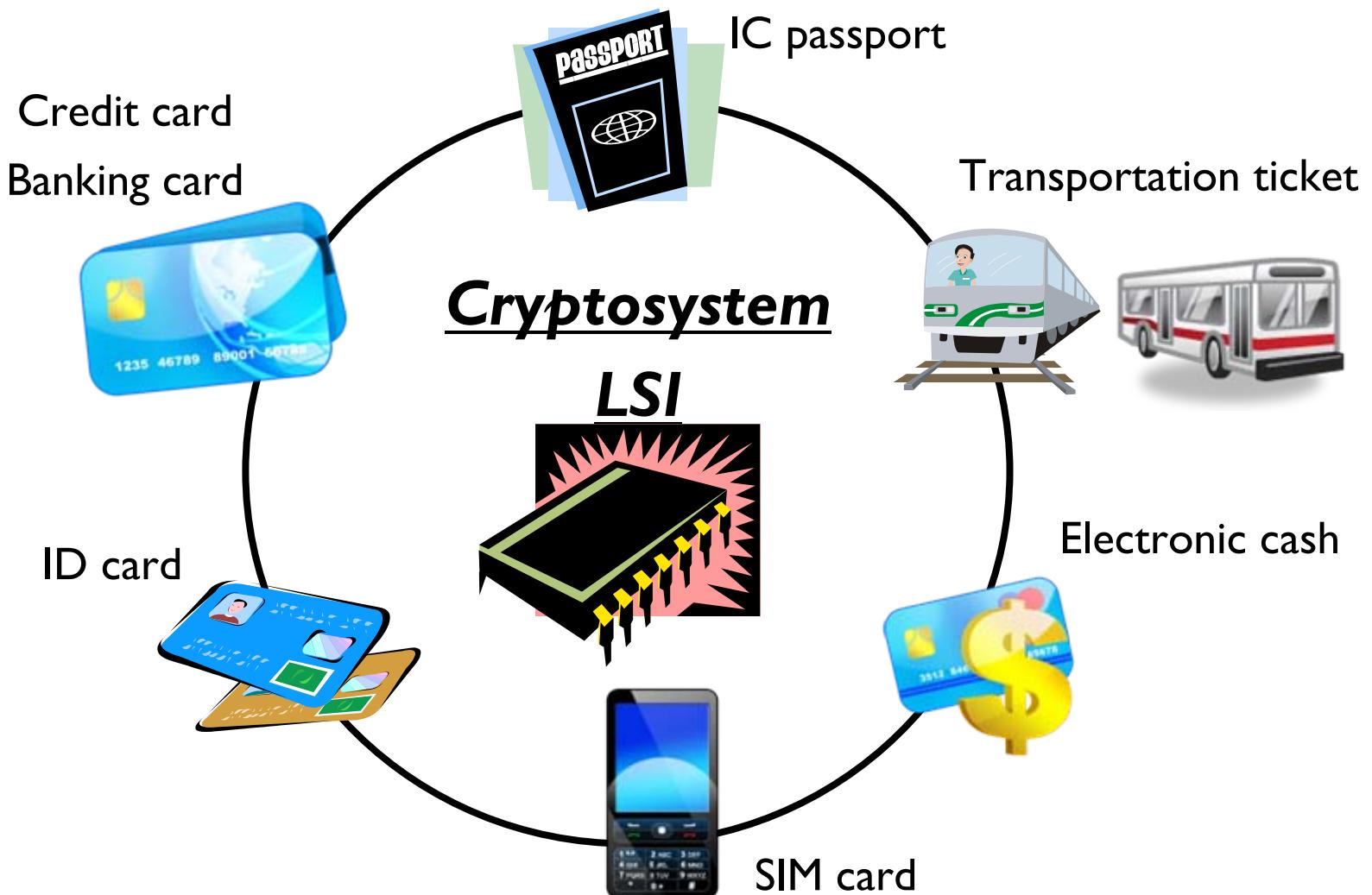
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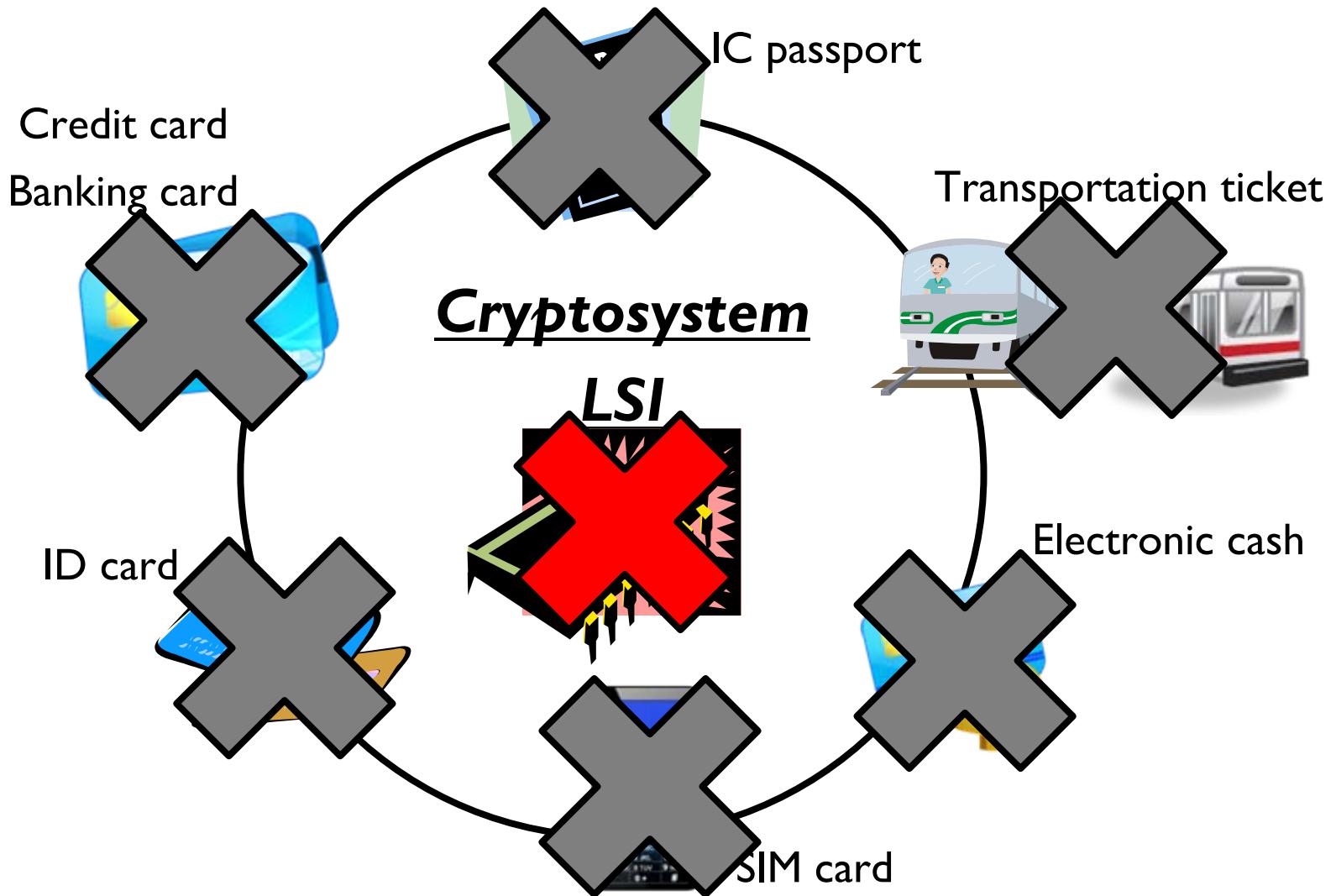
Outline

- ▶ Background
- ▶ Scan-based attacks
- ▶ Elliptic curve cryptosystem(ECC)
- ▶ Scan-based attack against ECC
- ▶ Experiments and results
- ▶ Conclusion

Background – Cryptosystem LSI –

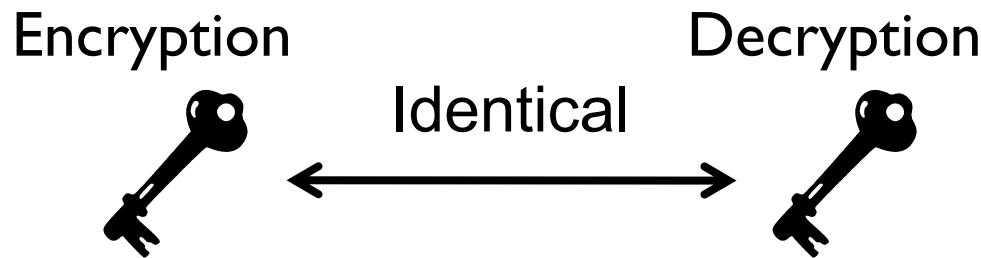


Background – Cryptosystem LSI –

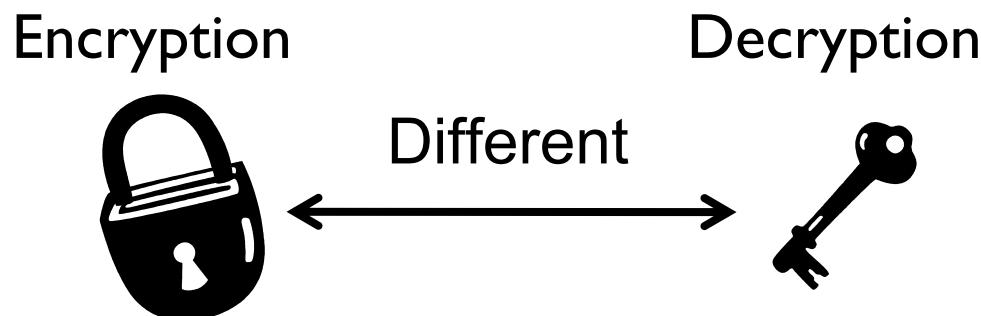


Background – Symmetric vs. Public –

- ▶ Symmetric-key cryptosystem: DES, AES



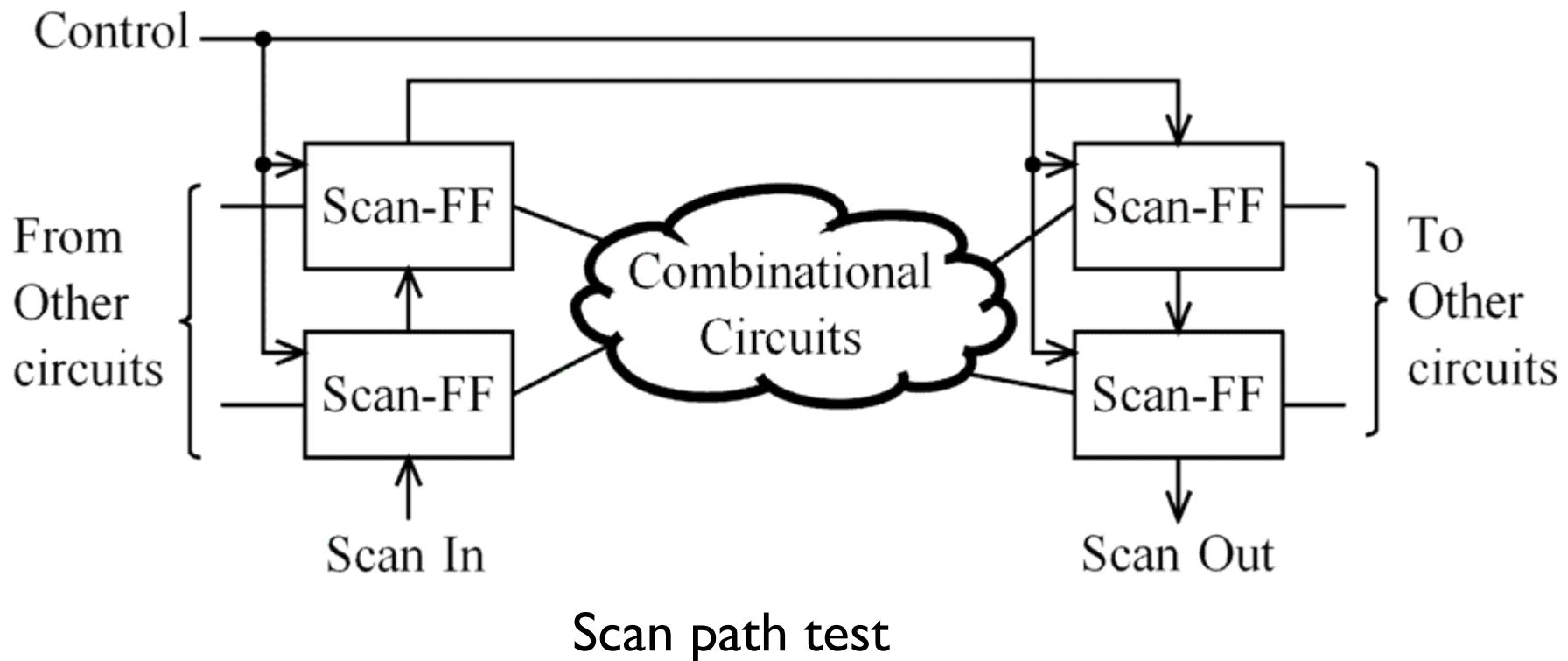
- ▶ Public-key cryptosystem: RSA, **ECC***



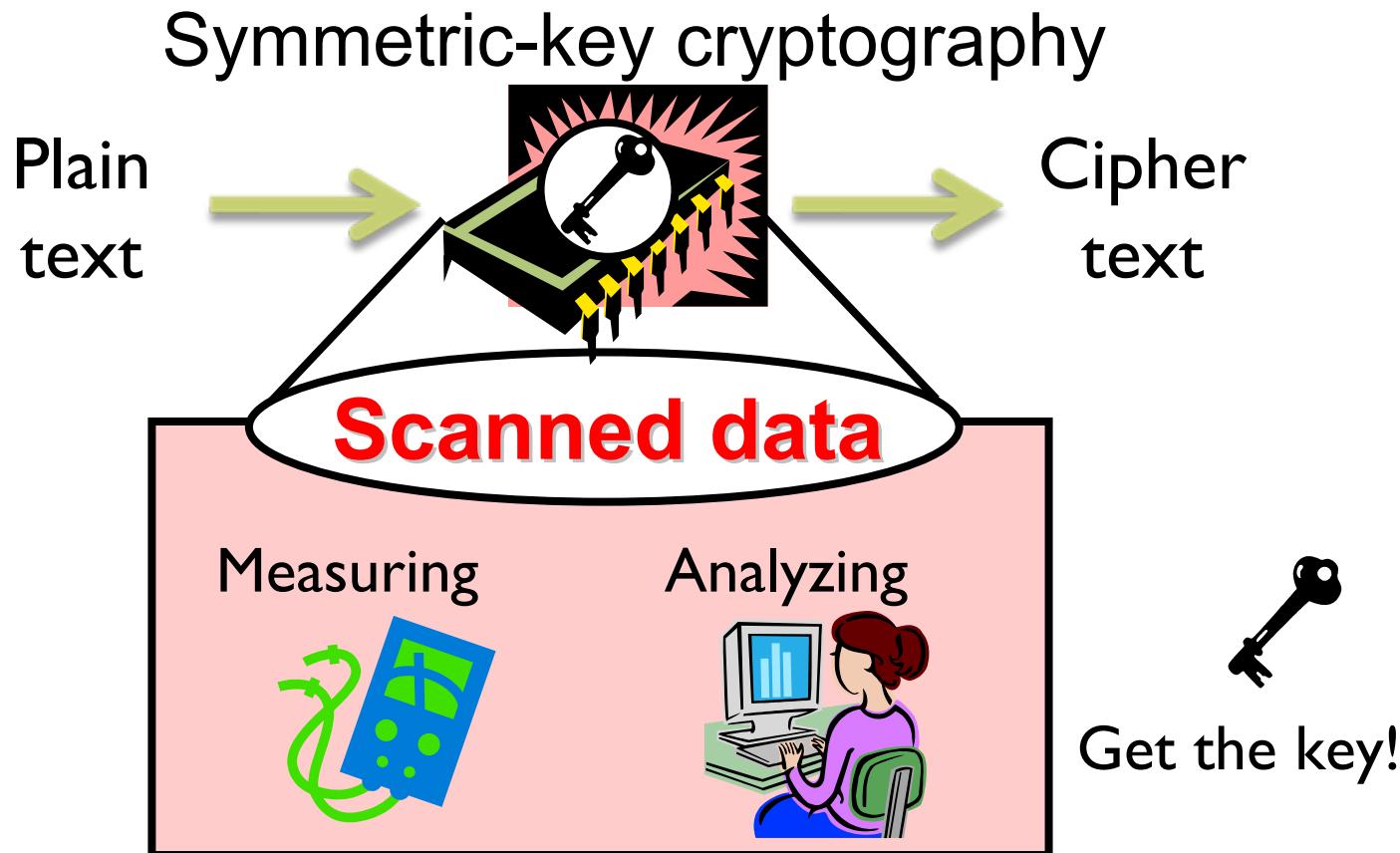
***ECC: Elliptic curve cryptosystem**

Background – Scan path test –

- ▶ High test efficiency
- ▶ Easy to implement



Scan-based attacks against DES[1] and AES[2,3]



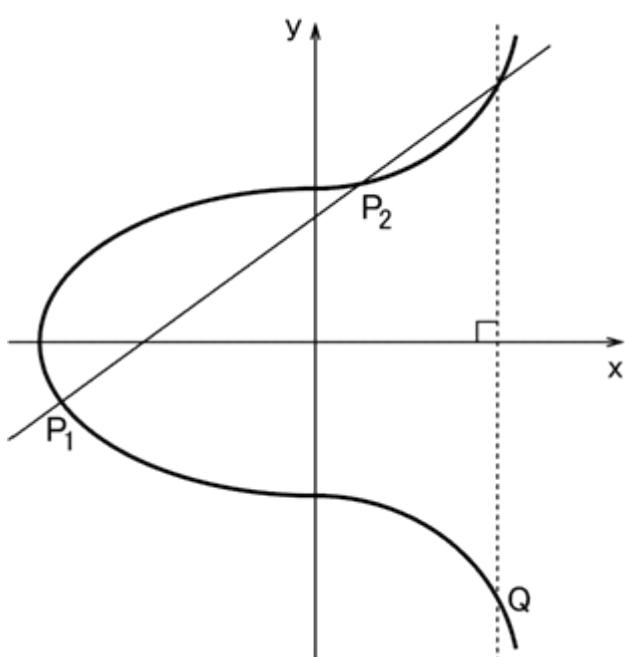
- [1] B. Yang, et al., International Test Conference, 2004.
- [2] B. Yang, et al., Design Automation Conference(DAC), 2005.
- [3] R. Nara, et al., IEICE, E92-A, No.12, Dec. 2009.

Purpose of our presentation

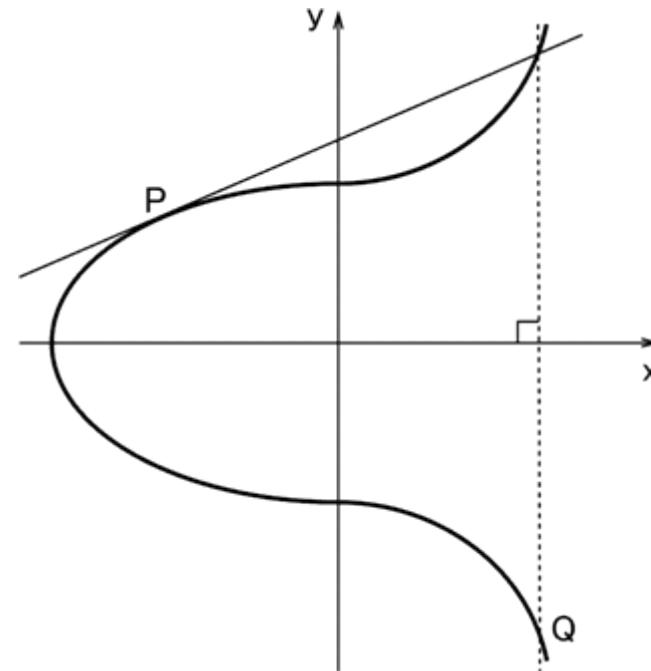
**Scan-based attack
against
elliptic curve cryptosystem**

Elliptic curve cryptosystem

► Basic operation



$$Q = P_1 + P_2$$



$$Q = 2P$$

Elliptic curve cryptosystem: $Q=kP$

▶ Montgomery's method [15]

$$k = (k_{m-1} k_{m-2} \dots k_{i+1} \boxed{k_i} \dots k_1 k_0), k_j \in \{0, 1\}$$

$$\begin{aligned} & [lP, (l+1)P] \xrightarrow{\quad} [2lP, (2l+1)P] \text{ if } k_i=0 \\ *l = k_{m-1} k_{m-2} \dots k_{i+1} & \xrightarrow{\quad} [(2l+1)P, 2(l+1)P] \text{ if } k_i=1 \end{aligned}$$

$k_3=1$	$P, 2P$	
$k_2=1$	$3P, 4P$	$P+2P = 3P, 2 \times 2P = 4P$
$k_1=0$	$6P, 7P$	$2 \times 3P = 6P, 3P+4P = 7P,$
$k_0=1$	$13P, 14P$	$6P+7P = 13P(1101_2 P), 2 \times 7P = 14P$

[15] P. L. Montgomery, Mathematics of Computation, 1987.

Intermediate values vs. secret key $k[18]$

If $k = (\underbrace{k_{m-1} k_{m-2} \dots k_{i+1}}_{\text{already-known}} | \boxed{k_i} \dots k_1 k_0)$, $k_j \in \{0, 1\}$

iff $k_i = 0$, $V(i)P \in$ a set of intermediate values

$$V(i)P = \left(\sum_{j=i}^{m-1} k_j 2^{j-i+1} + 1 \right) P$$

$$k_i \Leftrightarrow V(i)P$$

Sample

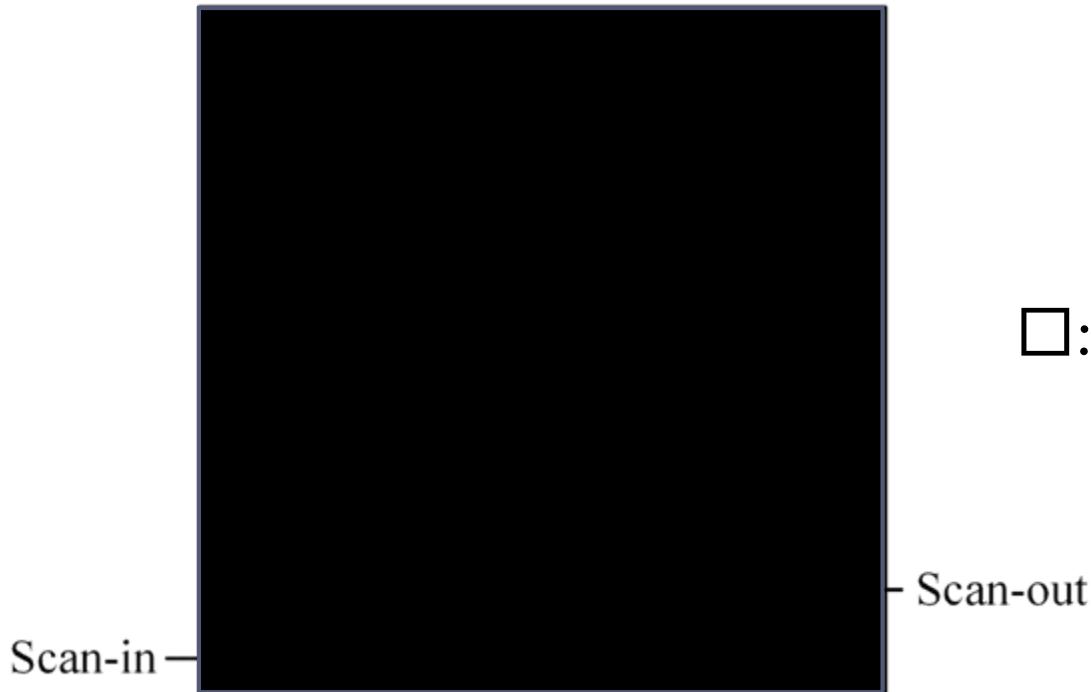
Intermediate values $V(i)P$ (4 bits)

k_3	I							
k_2	5P						7P	
	0(10)						I(II)	
k_1	9P		I IP		I3P		I5P	
	0(100)		I(101)		0(110)		I(111)	
k_0	8P	9P	IOP	IIP	I2P	I3P	I4P	I5P
	1000	1001	1010	1011	1100	1101	1110	1111

P, 2P
3P, 4P
6P, 7P
13P, I4P

1. $k_3 = 1$
2. $V(2)P = \underline{5P}$ does not exit $\rightarrow k_2 = 1$
3. $V(1)P = \underline{13P}$ exits $\rightarrow k_1 = 0$
4. $k_0=0?$ or $k_0=1?$ $\rightarrow \underline{13(1101)P}$

How to find the intermediate values?

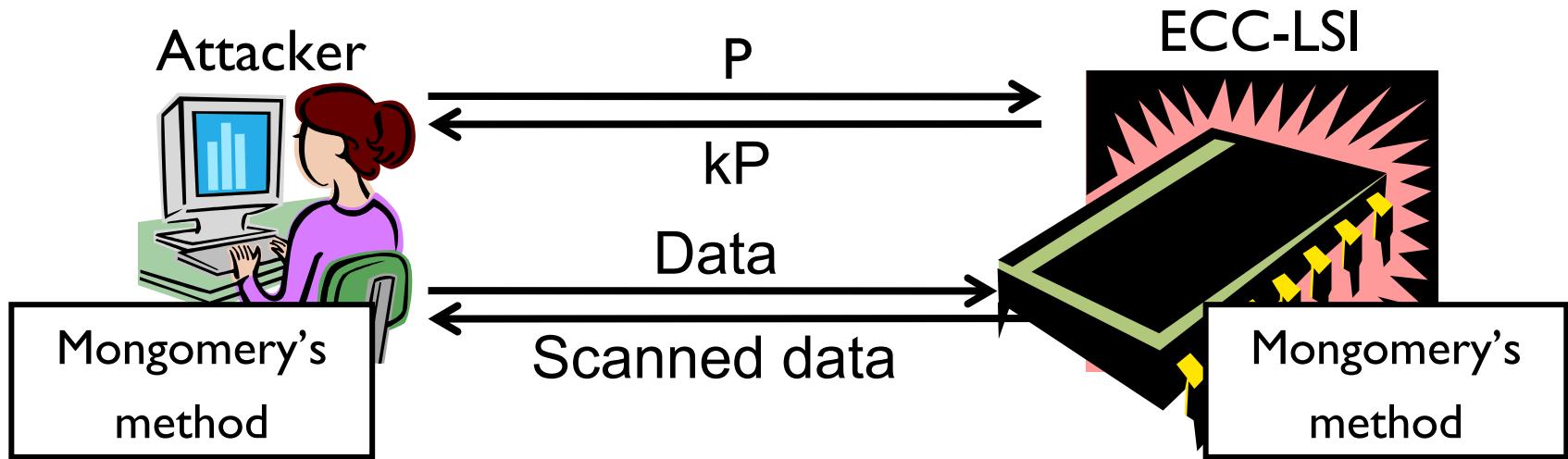


Layout of ECC LSI
□:Cell gate, ■:Register

- ▶ Problem 1: Which registers store intermediate values?
- ▶ Problem 2: When do intermediate values appear?

Assumption

- ▶ Attackers can
 - ▶ compute kP with any P by using an ECC LSI
 - ▶ access the scan path
- ▶ Known
 - ▶ kP algorithm used in an ECC LSI (e.g. Montgomery's method)

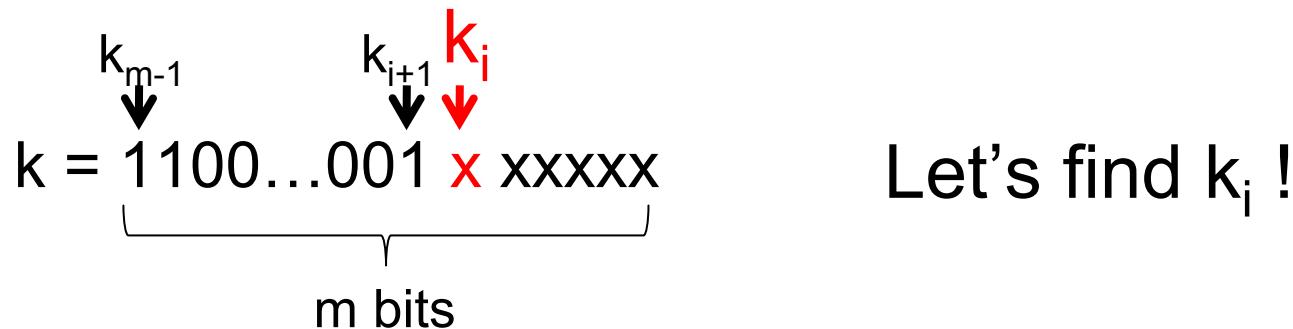


Proposed method

- ▶ **Discriminator to $V(i)P$ generated from *l-bit register value***

l-bit register is

- ▶ Independent of the connection of registers
- ▶ Independent of timing



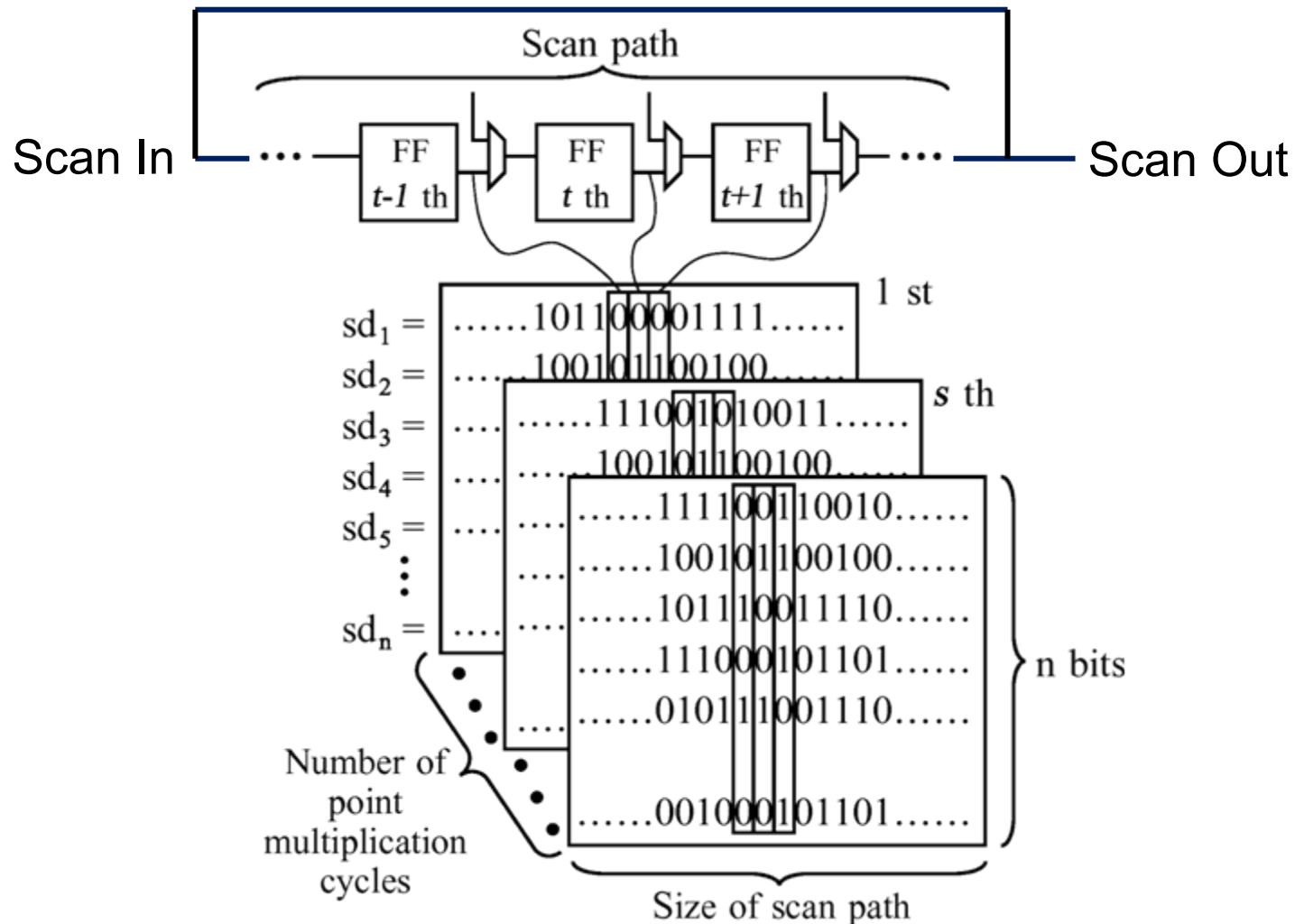
D_i : Discriminator to $V(i)P$

Input: $P_r \in E(F_{2^m})$ ($1 \leq r \leq n$), $V(i)$

Output: Discriminator D_i

$V(i)P_1 = 0$	1	$0 \dots 1$	0	1	$1 \dots 1$	1	{ } \left. \begin{array}{l} \\ \\ \\ \\ \\ \end{array} \right\} D_i \\ (n \text{ bits})
$V(i)P_2 = 1$	1	$0 \dots 1$	1	1	0	$1 \dots 0$	
$V(i)P_3 = 1$	1	$0 \dots 0$	1	0	$1 \dots 1$	1	
$V(i)P_4 = 0$	1	$1 \dots 0$	1	1	$0 \dots 0$	0	
$V(i)P_5 = 1$	0	$1 \dots 1$	1	1	$1 \dots 1$	0	
\vdots	\vdots				\vdots		
$V(i)P_n = 0$	1	$0 \dots 0$	1	0	$1 \dots 0$		
$\underbrace{\hspace{1cm}}$				$\underbrace{\hspace{1cm}}$			
$y\text{-coordinate}$				$x\text{-coordinate}$			
$2m \text{ bits}$							

Scanned data



D_i exists?

Input: $P_r \in E(F_{2^m})$ ($1 \leq r \leq n$), $V(i)$

Output: Discriminator D_i

$V(i)P_1 = 0 \ 1 \ 0 \dots 1 \ 0 \ 1 \ 1 \dots 1$

$V(i)P_2 = 1 \ 1 \ 0 \dots 1 \ 1 \ 0 \ 1 \dots 0$

$V(i)P_3 = 1 \ 1 \ 0 \dots 0 \ 1 \ 0 \ 1 \dots 1$

$V(i)P_4 = 0 \ 1 \ 1 \dots 0 \ 1 \ 1 \ 0 \dots 0$

$V(i)P_5 = 1 \ 0 \ 1 \dots 1 \ 1 \ 1 \ 1 \dots 0$

\vdots

$V(i)P_n = 0 \ 1 \ 0 \dots 0 \ 1 \ 0 \ 1 \dots 0$

$\underbrace{\hspace{1cm}}_{\text{y-coordinate}}$ $\underbrace{\hspace{1cm}}_{\text{x-coordinate}}$

$\left. \begin{array}{c} D_i \\ (n \text{ bits}) \end{array} \right\}$

D_i

$sd_1 = \dots 11110011010\dots$

$sd_2 = \dots 100101100100\dots$

$sd_3 = \dots 101110011110\dots$

$sd_4 = \dots 111000101101\dots$

$sd_5 = \dots 010111001110\dots$

\vdots

$sd_n = \dots 001000101101\dots$

$\left. \begin{array}{c} n \text{ bits} \\ \text{Size of scan path } x \\ \text{Number of point multiplication cycles} \end{array} \right\}$

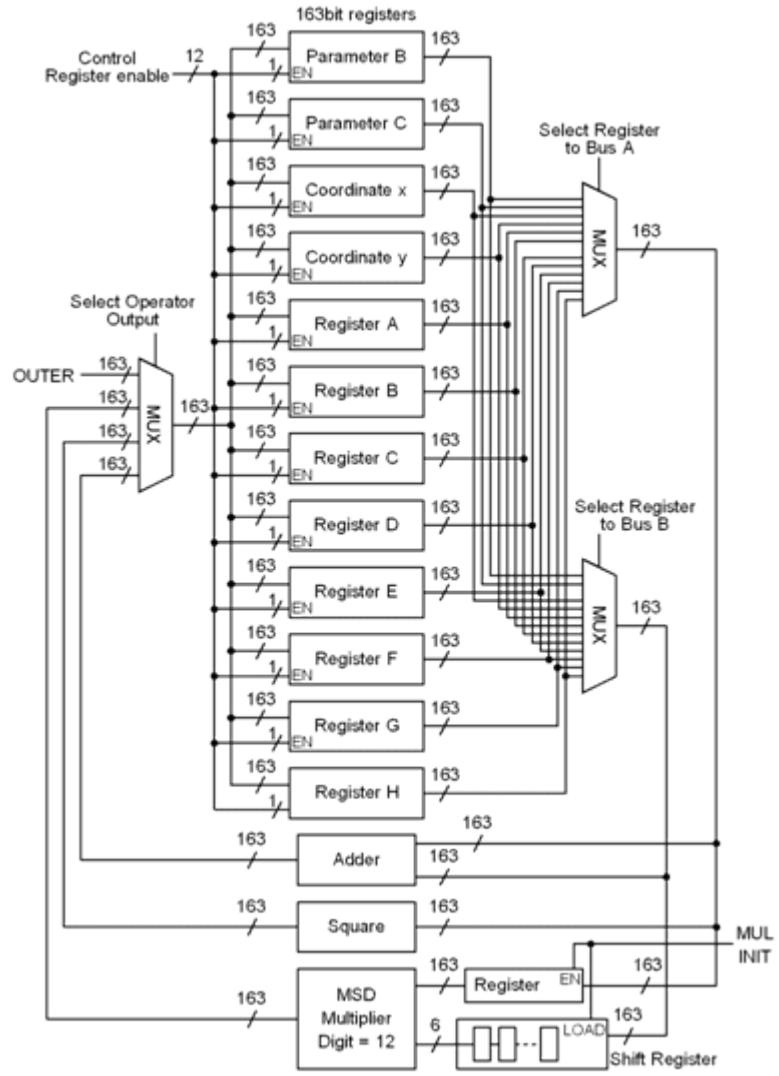
D_i exists $\Leftrightarrow k_i=0$

D_i does not exist $\Leftrightarrow k_i=1$

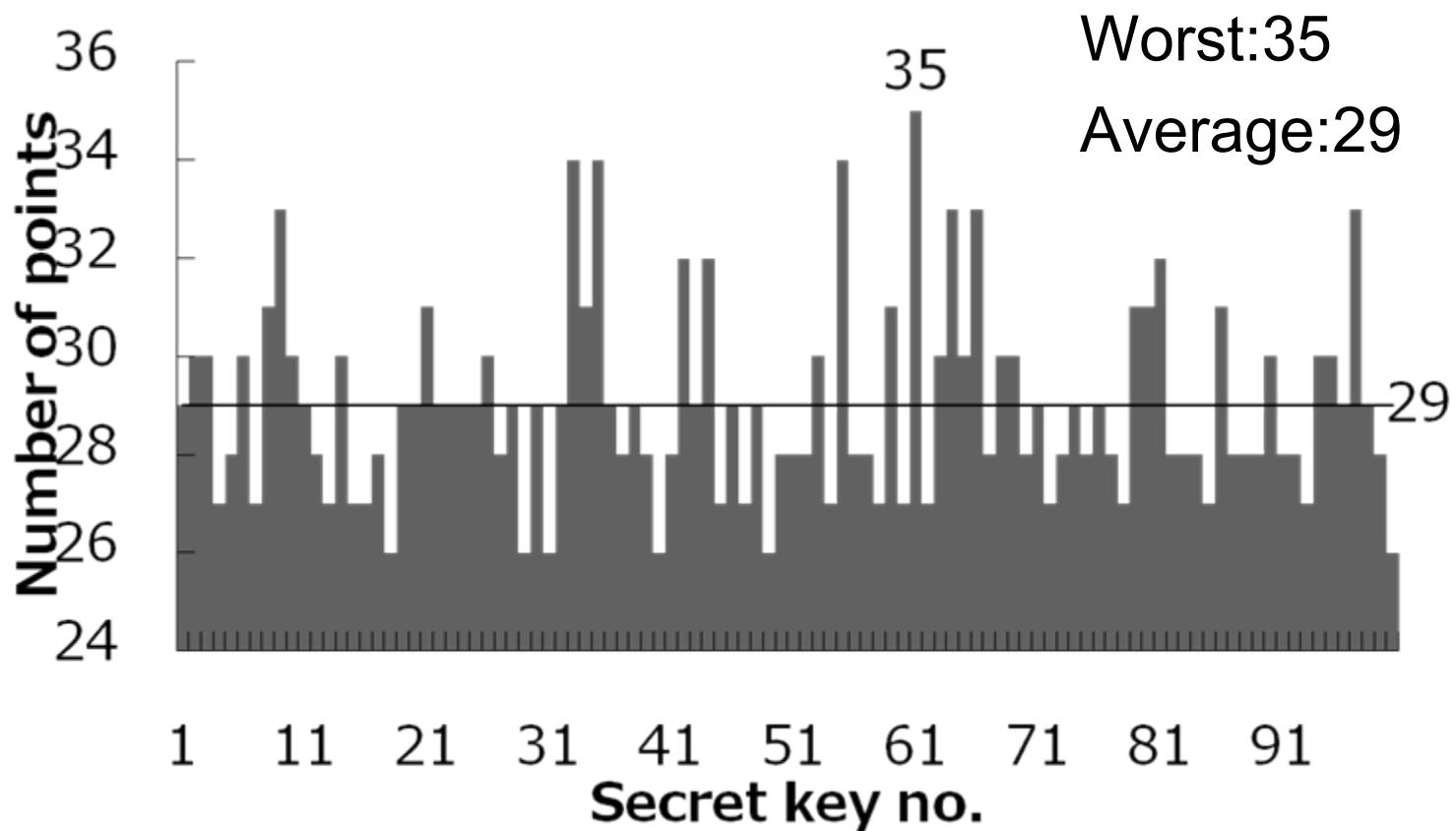
Experiments

- ▶ Key length: **163** bits
- ▶ Size of scan path : **2,520** bits
- ▶ Q=kP: **15,137** cycles

ECC-LSI architecture



Results



Conclusion

- ▶ **Scan-based attack** against **elliptic curve cryptosystem**
- ▶ Deciphering a secret key k
at **40 seconds** by using **29 input**

Future works

- ▶ Attacks :
 - Method for accessing scan path
 - Compactor
- ▶ Defense :
 - Efficiency defense method