

Efficient Sensitivity-Based Capacitance Modeling for Systematic and Random Geometric Variations

16th Asia and South Pacific Design Automation Conference

Nick van der Meijs

CAS, Delft University of Technology, Netherlands

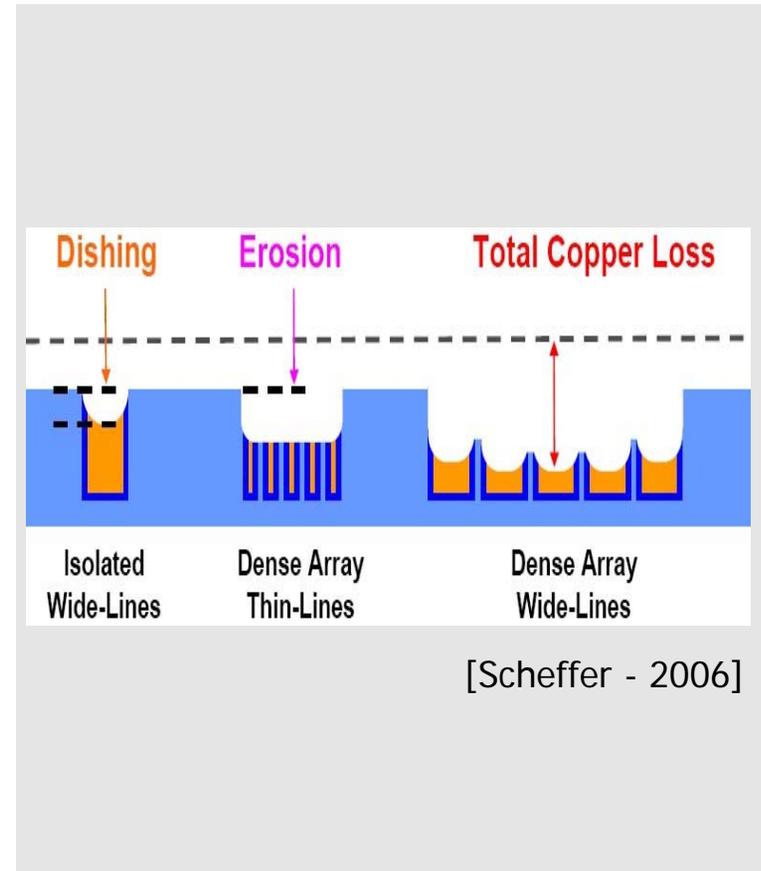
January 26, 2011 – Yokohama, Japan

Outline

- Introduction
 - Systematic & random variations
 - Modeling method (panel sensitivity)
- Modeling for systematic variations
- Modeling for random variations
 - Panel sensitivity based statistical modeling method
 - Experiments and results
 - Case study: 8-bit binary-scaled charge-redistribution DAC
- Modeling for both variations
 - Diagram
 - Experiment and result
- Conclusion

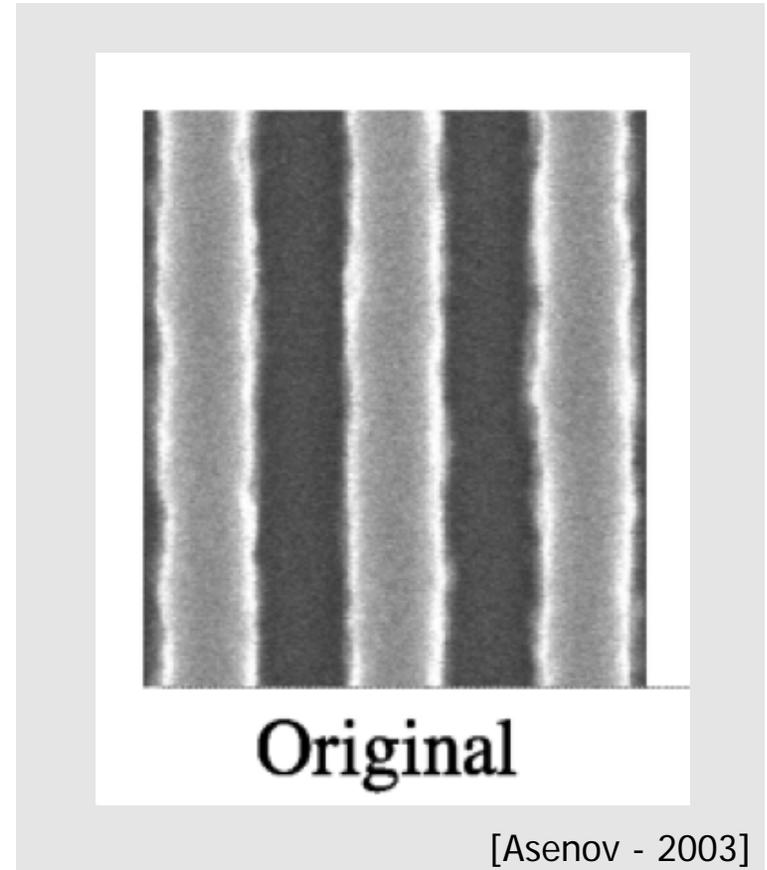
Process Variability

- Systematic Variation
 - Lithography, Etching, CMP
 - Layout dependent
- Random Variation



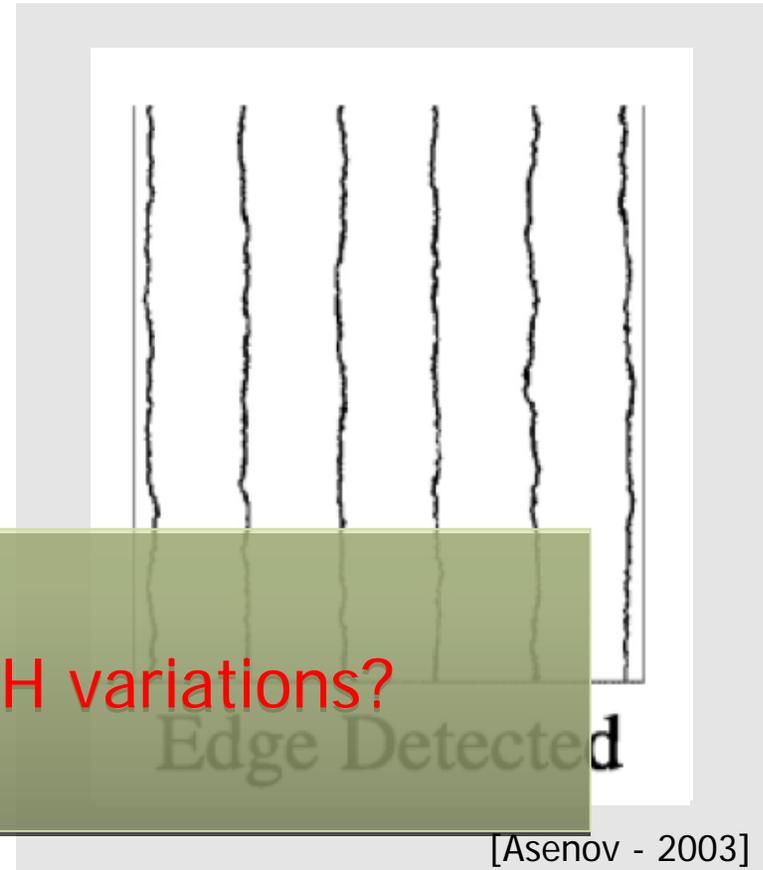
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 - Line-edge roughness
 - Spatial correlation



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Variabilities of R & C I

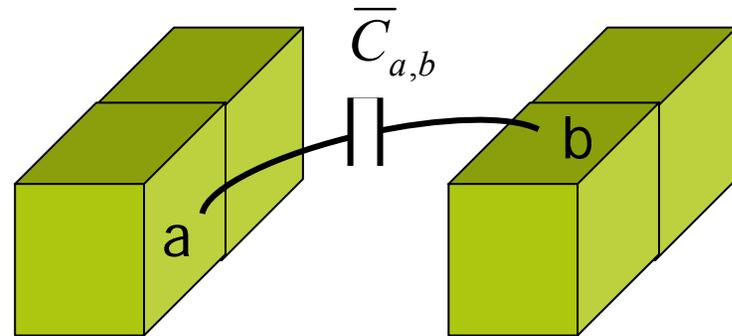
? How to model BOTH variations?

Edge Detected

[Asenov - 2003]

Modeling Method

- BEM-based capacitance extraction
 - Partial short-circuit capacitances \bar{C}
 - Nominal capacitance C_0 : sum of associated partial short-circuit capacitances \bar{C}



Modeling Method

- Panel sensitivity

have been produced in calculation
of the nominal C_0 using BEM

$$S_{k,ij} = \frac{\partial C_{ij}}{\partial \rho_k} = -\frac{1}{\epsilon A_k} \sum_{a \in N_i} \sum_{b \in N_j} \bar{C}_{k,a} \bar{C}_{k,b}$$

[Bi-CICC-2009]

- ρ_k : a small displacement of a panel k
- ϵ : material permittivity around panel k
- A_k : the area of panel k
- $\bar{C}_{k,a}$: an entry in the partial short-circuit capacitance matrix
- $\sum_{a \in N_i} \bar{C}_{k,a}$: capacitance between a panel k and a node i

Modeling Method

- Panel sensitivity

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[Bi-CICC-2009]

- no extra costly computation
- partial capacitances
(data for standard capacitance extraction)
- BEM
- **FAST!**

Modeling for Systematic Variation

- Linear / sensitivity model

$$\mathbf{C} = \mathbf{C}_0 + \sum_i \frac{\partial \mathbf{C}}{\partial \mathbf{p}_i} \cdot \Delta \mathbf{p}_i$$

$$\frac{\partial \mathbf{C}_{ij}}{\partial \mathbf{p}} = \sum_{k \in s_p} \mathbf{S}_{k,ij}$$

- s_p : the set of panels incident to the geometric parameter p

Modeling for Systematic Variation

- Linear / sensitivity model

$$\mathbf{C} = \mathbf{C}_0 + \sum_i \frac{\partial \mathbf{C}}{\partial \mathbf{p}_i} \cdot \Delta \mathbf{p}_i$$

$$\frac{\partial \mathbf{C}_{ij}}{\partial \mathbf{p}} = \sum_{k \in s_p} \mathbf{S}_{k,ij}$$

- s_p : the set of panels incident to the geometric parameter p
- Variance of capacitance due to the dimensional variation:

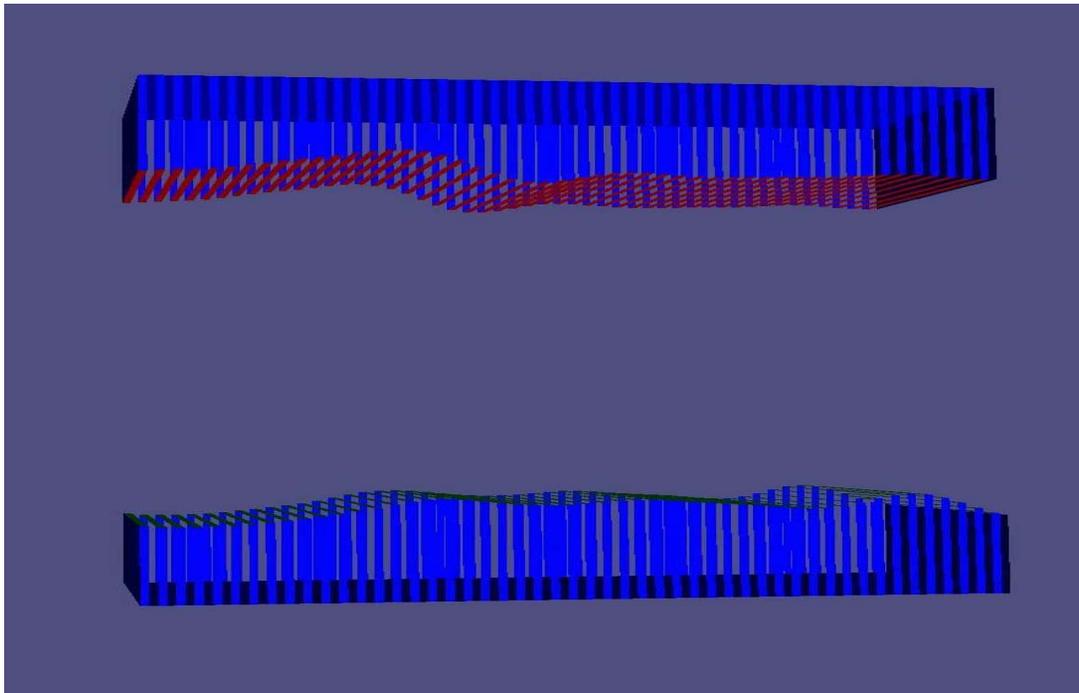
$$\text{var}(\mathbf{C}_{ij})_{\text{sys}} = \left(\sum_{k \in s_p} \mathbf{S}_{k,ij} \right)^2 \sigma_p^2$$

[Bi-CICC-2009]

- σ_p : standard deviation of parameter p

Modeling for Random Variations

- Modeling the effects of Line-Edge Roughness (LER) on capacitances



Statistical Model of Capacitances

- Capacitance Modeling

$$\Delta \mathbf{C} = \sum_{l=1}^L \sum_{i=1}^{n_l} \mathbf{S}_i \boldsymbol{\rho}_i$$

- $\Delta \mathbf{C}$: capacitance variation induced by the LER
- $\boldsymbol{\rho}$: a sequence of random variables (panel displacements)
- \mathbf{S}_i : panel sensitivity associated with panel displacement $\boldsymbol{\rho}_i$
- n_l : the number of deviation panels for rough line l
- L : the number of rough lines

Statistical Model of Capacitances

- Capacitance Modeling

$$\Delta C = \sum_{l=1}^L \sum_{i=1}^{n_l} S_i \rho_i$$

- Variance of ΔC

$$\sigma_{\Delta C}^2 = \text{var}\left(\sum_{l=1}^L \sum_{i=1}^{n_l} S_i \rho_i\right)$$

*Some ρ_i s are **NOT** independent !!*

Statistical Model of Capacitances

- Capacitance Modeling

$$\Delta \mathbf{C} = \sum_{l=1}^L \sum_{i=1}^{n_l} \mathbf{S}_i \rho_i$$

- Variance of $\Delta \mathbf{C}$

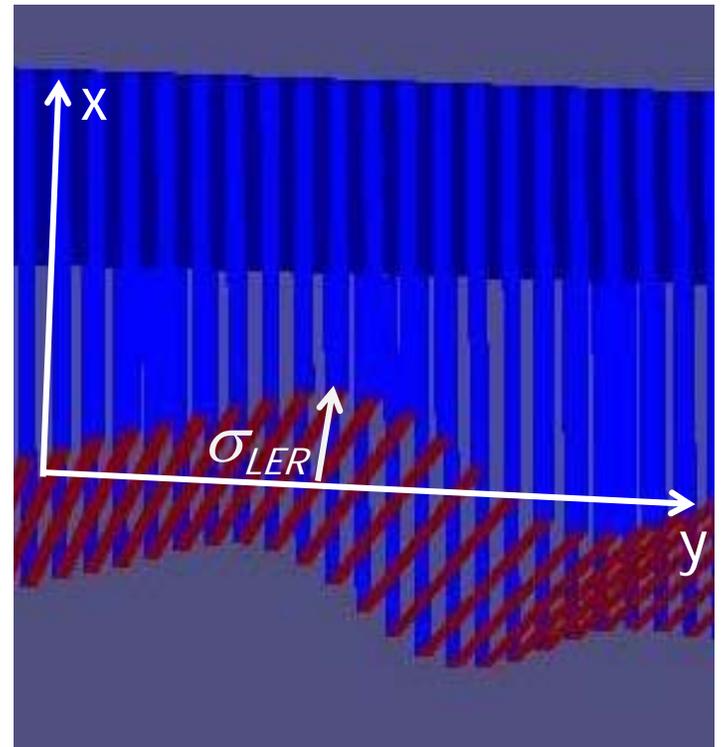
$$\sigma_{\Delta \mathbf{C}}^2 = \text{var}\left(\sum_{l=1}^L \sum_{i=1}^{n_l} \mathbf{S}_i \rho_i \right)$$

$$\sigma_{\Delta \mathbf{C}}^2 = \sum_{l=1}^L \left[\sum_{i=1}^{n_l} s_i^2 \text{var}(\rho_i) + 2 \sum_{i,j:i < j} s_i s_j \text{cov}(\rho_i, \rho_j) \right]$$

Random Variation

$$\sigma_{\Delta C}^2 = \sum_{l=1}^L \left[\sum_{i=1}^{n_l} s_i^2 \underline{\text{var}(\rho_i)} + 2 \sum_{i,j:i < j} s_i s_j \text{cov}(\rho_i, \rho_j) \right]$$

$$\text{var}(\rho_i) = \sigma_{LER}^2$$

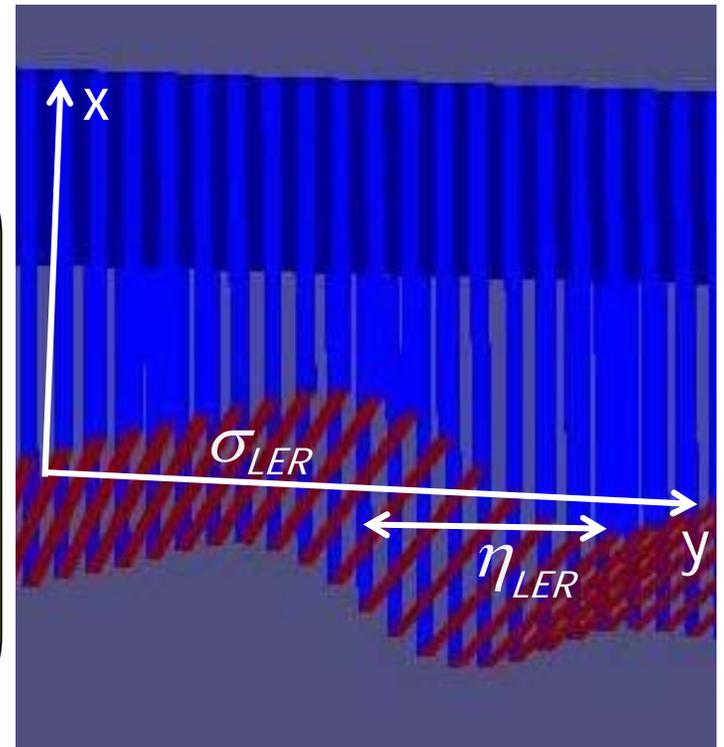


Random Variation

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$$\text{var}(\rho_i) = \sigma_{LER}^2$$

Any correlation function!



Random Variation

$$\sigma_{\Delta C}^2 = \sum_{l=1}^L \left[\sum_{i=1}^{n_l} s_i^2 \text{var}(\rho_i) + 2 \sum_{i,j:i<j} s_i s_j \text{cov}(\rho_i, \rho_j) \right]$$

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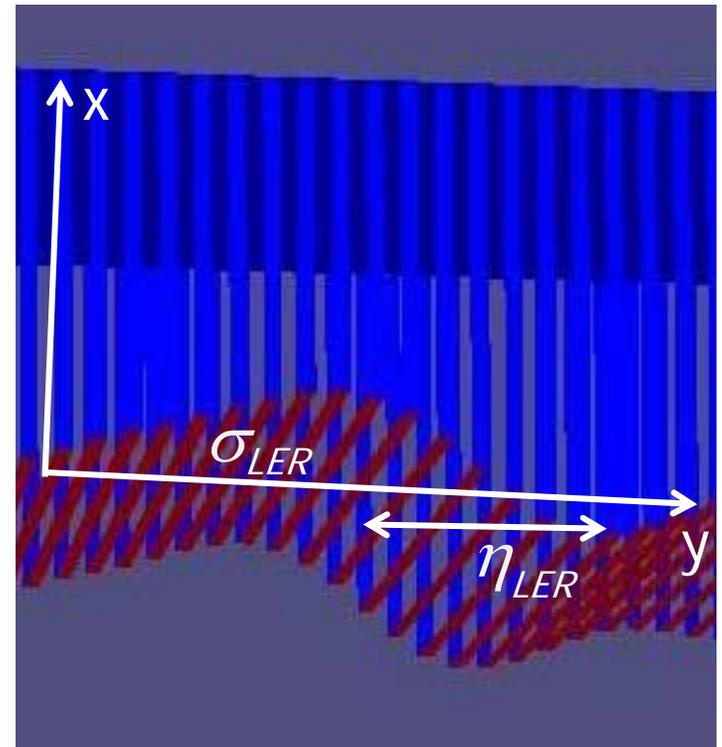
- Gaussian correlation function:

$$\text{cov}(\rho_i, \rho_j) = \sigma_{LER}^2 \exp\left(-\frac{|r_{i,y} - r_{j,y}|^2}{\eta_{LER}^2}\right)$$

$r_{i,y}$ is the y-coordinate of the position associated with ρ_i

η_{LER} is the correlation length in y-direction

Two characterization parameters

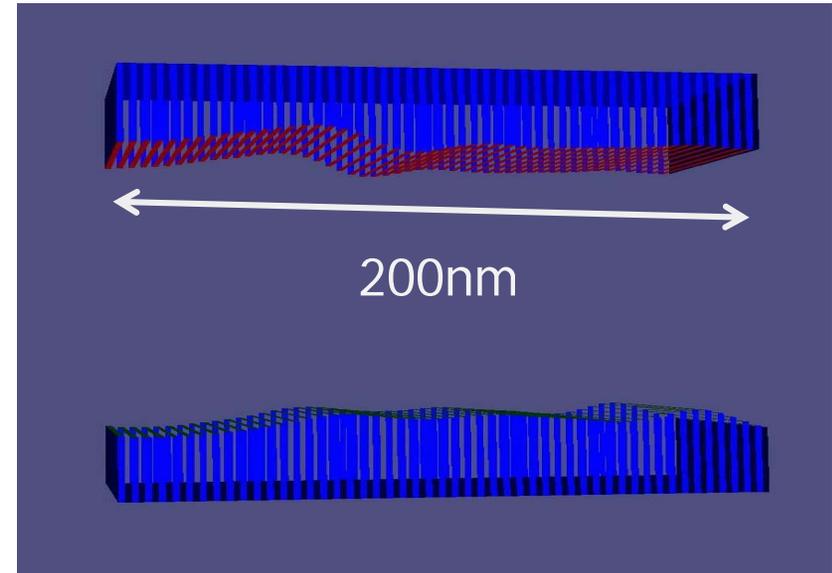


Experiment - I

$$\sigma_{LER} = 3.5nm \quad \eta_{LER} = 16nm$$

- Measurement data from IMEC
[Stucchi - 2007]

- Relative std. deviation: $\frac{\sigma_c}{C}$
- Monte-Carlo simulation
 - 1000 samples

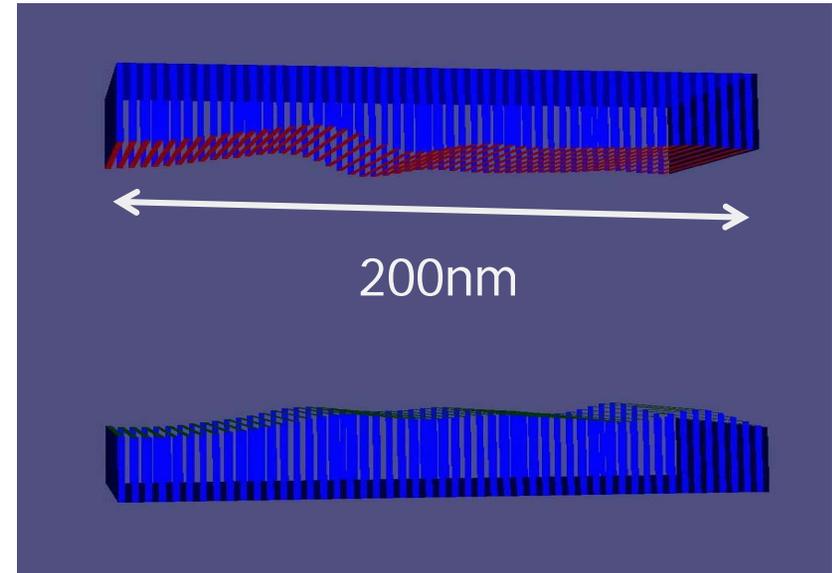


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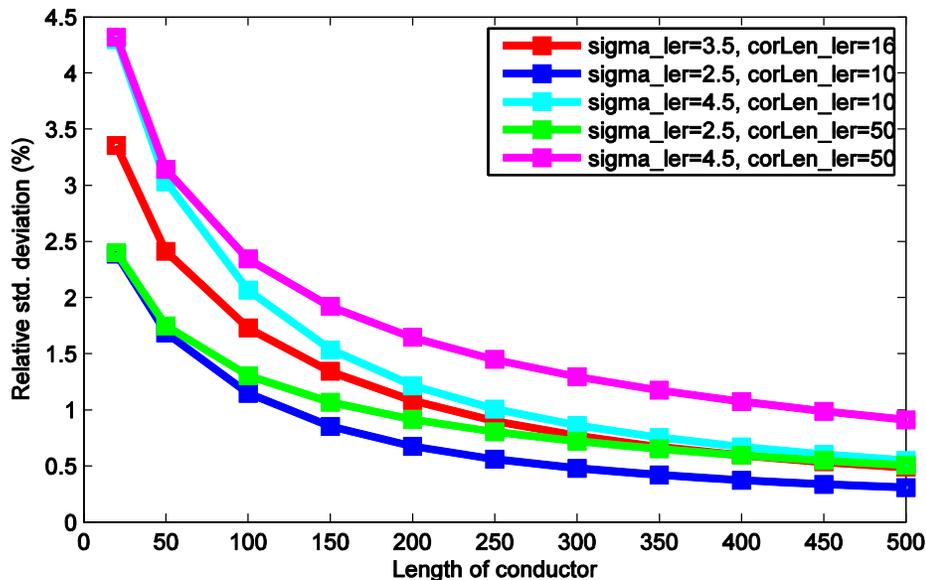


	σ_c / C	Error	CPU Time
MC simulation	0.681%	-	48653''
Proposed model	0.603%	11.5%	50''

Experiment - II

Using the proposed model, one can easily study:

1. The relationship between $\frac{\sigma_C}{C}$ and the conductor length;
2. The impact of parameters σ_{LER} and η_{LER} on $\frac{\sigma_C}{C}$.

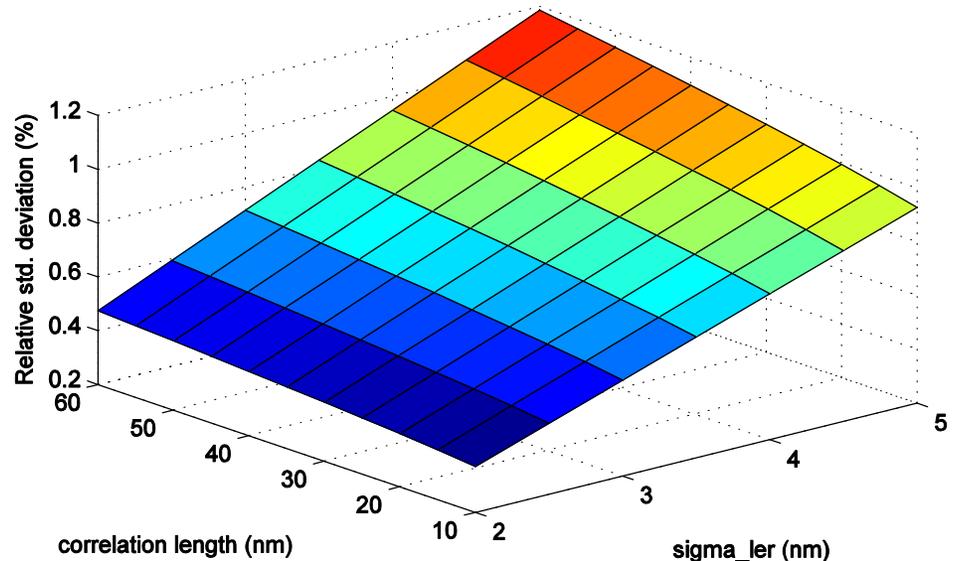
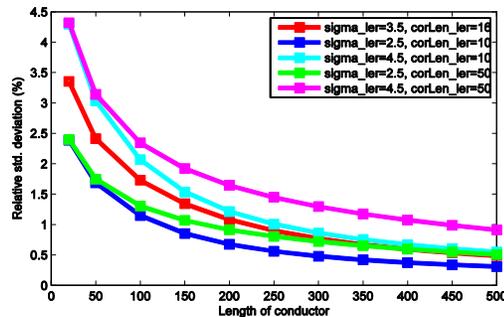


- Same structure as Experiment-I
- 5 examples of mismatches
- Combination of various σ_{LER} and η_{LER}
- Application:
 - High accuracy vs. low power consumption

Experiment - II

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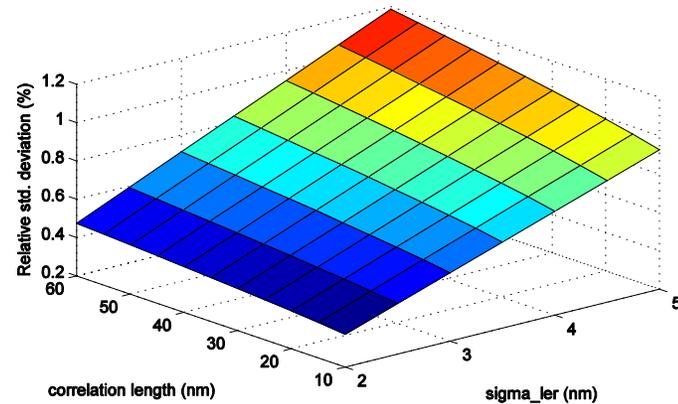
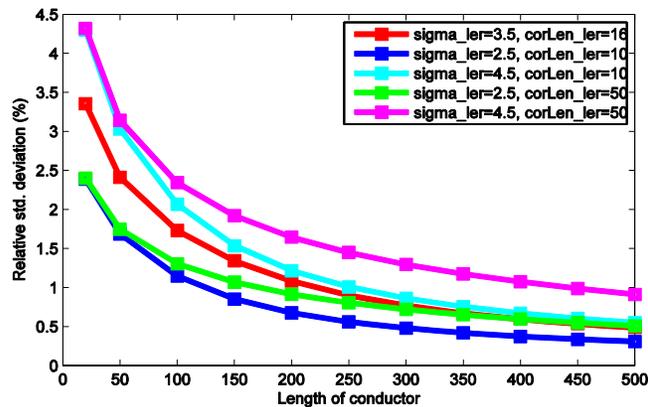


- Two sweeping parameters
- Proposed method: an hour
- MC approach: 43 days

Experiment - II

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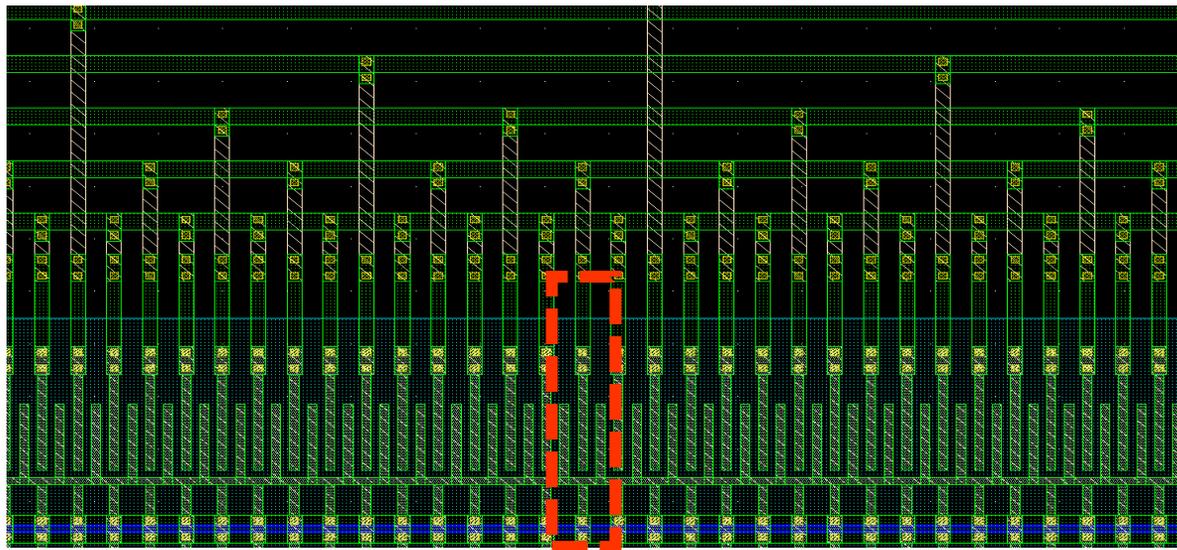
1. The relationship between $\frac{\sigma_C}{C}$ and the conductor length;
2. The impact of parameters σ_{LER} and η_{LER} on $\frac{\sigma_C}{C}$.



The proposed modeling method provides a fast and practical tool for circuit designers to estimate mismatches and optimize dimensions of critical structures accordingly.

A Case Study

- Novel passive devices with high-precision structures
- 8-bit charge-redistribution DAC
 - 255 identical unit capacitors
 - Min. value of a unit capacitor for high power efficiency (0.5fF)
 - Main consideration: mismatch of capacitors ($\frac{\sigma_{C_0}}{C_0}$)

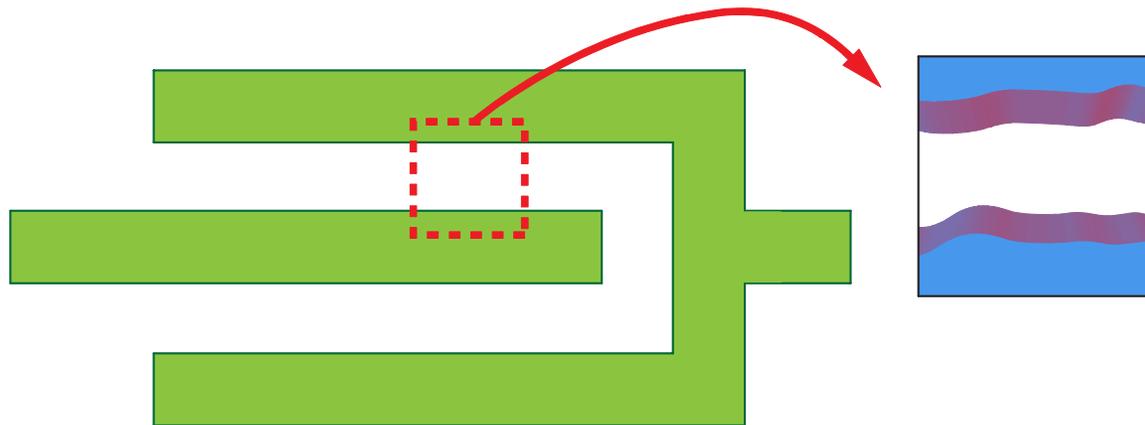


Unit cap.

[Harpe-ESSCIRC-2010]

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- Design requirement: mismatch of the unit capacitor $< 1\%$

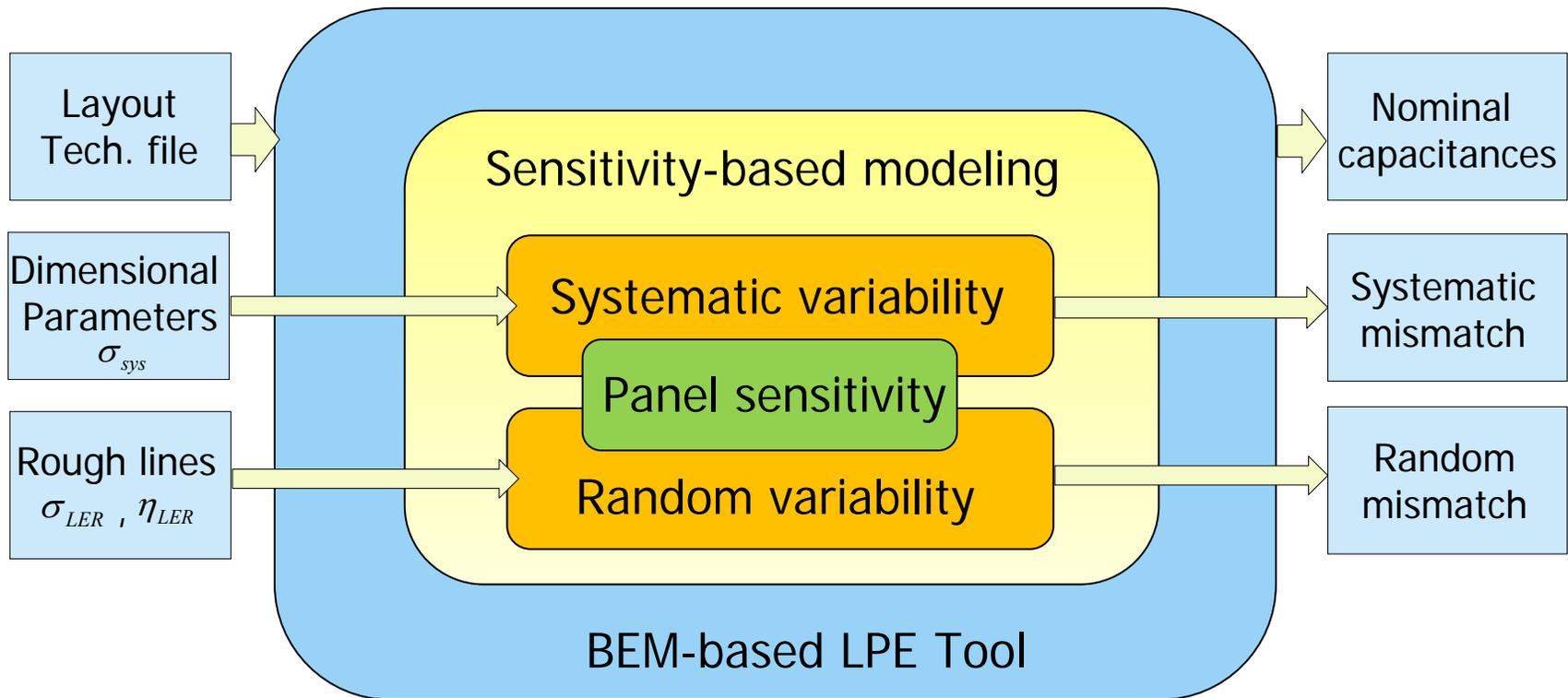
A Case Study

- Design requires:
 - mismatch of the unit capacitor $< 1\%$
- Simulation shows:
- The mismatch of the unit capacitor caused by the LER is around **0.25%**;
- Measurement indicates:
- A random mismatch of the unit capacitor being **better than 0.6%**;
- Simulation and measurement together conclude:
- Simulation results are very reasonable;
- The structure can be used for more accurate designs: e.g. 10-bit DAC (designer's plan!);

Design tool enables a new design,
based on proper modeling but NOT guessing

Sensitivity Based Modeling for Both Systematic and Random Variations

Design For Manufacturing



Experiment - III

- Two parallel conductors
- width/space = $2\mu\text{m}/2\mu\text{m}$; thickness = $2\mu\text{m}$; length = $8\mu\text{m}$
- LER on four edges of two conductors:
 $\sigma_{LER} = 0.03\mu\text{m}$, $\eta_{LER} = 2.00\mu\text{m}$
 $\sigma_{LER} = 0.04\mu\text{m}$, $\eta_{LER} = 2.88\mu\text{m}$
- Systematic variation of the two conductors:
 $\sigma_{sys} = 0.03\mu\text{m}$, $\sigma_{sys} = 0.04\mu\text{m}$
- Parameters are chosen based on pure assumption

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- Systematic variation of the two conductors:
 $\sigma_{sys} = 0.03\mu\text{m}$, $\sigma_{sys} = 0.04\mu\text{m}$
- Parameters are chosen based on pure assumption
- 3 Monte Carlo simulations with 1000 samples each
 - Systematic variation
 - Random variation
 - Superposition of the above two

Experiment - III

	MC simulation	Proposed method
$\sigma_{C_{sys}} / C$	2.22%	2.05% (7.72% error)
$\sigma_{C_{LER}} / C$	0.21%	0.24% (14.23% error)
$\sigma_{C_{sNr}} / C$	2.22%	2.06% (7.18% error)
CPU Time	38h52'	58''

- The systematic variation is the dominant one
- Some designs are sensitive to both variations and some (e.g. 8-bit DAC) are only vulnerable to random variations
- Being able to apply the appropriate modeling techniques is essential

Extremely high efficiency + good enough accuracy
= a fast and convenient tool for DFM !

Conclusion

- **Sensitivity based** method for statistical property of capacitances due to **both** systematic and random variations
- Modeling method for the effect of LER on capacitance
 - Simulations & **measurement on** chips
 - Good enough accuracy & **high efficiency**
 - Useful and convenient tool for **mismatch estimation** & **circuit optimization**
- Overall picture of the sensitivity based method for both variations
 - Extension of BEM LPE tools
 - Serving **DFM!**