Equivalent Lumped Element Model for n-Port Through Silicon Via Networks

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- Increased power consumption and delay due to increased wiring resistance and capacitance have become a major obstacle for further improving the performance of integrated circuits.
- 3D-integration using Through Silicon Vias (TSVs) is a promising solution for higher integration allowing for higher system speed and lower power consumption.
 - TSV technology, also, provides other advantages such as:
 - high interconnect density,
 - small footprint, and
 - heterogeneous integration of the various materials and technologies.



Introduction ⁽²⁾

- The characteristics of a TSV are dependent on its geometrical and electrical parameters.
- There is a number of configurations for TSV-based 3D IC integration.



An Example of "stacked" 3D IC with TSV



Typical TSV structure

(a) 3D view, (b) top view and (c) side view



The objective of this research is set to introduce a complete and robust model that accurately captures:

- all the loss modes of a TSV,
 - In conductor (resistance and skin effect)
 - In substrate (resistance)
- coupling parasitics (R, L and C) between TSVs,
- the TSV nonlinear capacitance and resistance of the depletion region (MOS) effect.



Modeling Methodology

(a) propose a physics-based lumped element model for two adjacent TSVs,

(b) simulate the structure of the two adjacent TSVs using EM simulation

(c) use the results of EM simulator to optimize the physics-based lumpedelement values under a number of different setups

(d) use the dimensional analysis method to develop closed-form expressions for the values of model lumped elements

(e) use the results of step (c) to optimize the coefficients of the closed-form expressions from (d)

(f) validate the model with its elements' value using the closed-form expressions of step (e) against EM simulation.



Physics-Based Proposed Model



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Circuit	Physical meaning
element	
R_0, L_0	Ohmic loss of the conductor
R_{1}, L_{1}	Skin effect of the conductor
C_{ox}	Capacitance of the oxide
C_{dep}, R_{dep}	Silicon substrate depletion region capacitance and resistance
C_{si}, R_{si}	Silicon substrate capacitance and resistance
C_c, R_c, L_m	Capacitive, resistive, and inductive coupling

Alaa El-Rouby, Equivalent Lumped Element Model for n-Port Through Silicon Via Networks, Jan-2011

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A Set of Test Multi-TSV Arrangements

The parasitics of two adjacent TSVs are investigated under different arrangements as shown below



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Characteristics of Multi-TSV Structures⁽²⁾



The equivalent resistance of a TSV for three test cases.

The equivalent Inductance of a TSV for three test cases

→ Results show that the TSV self resistance and inductance slightly affected (negligible) by the arrangement of the TSVs around it.



Characteristics of Multi-TSV Structures⁽³⁾



Capacitive coupling normalized w.r.t. the total capacitance of the TSV in the center.



Resistive coupling normalized w.r.t. the total resistance of the TSV in the center.



Inductive coupling normalized w.r.t. the total inductance of the central TSV.

- Capacitive and resistive coupling are weak
 insignificant beyond the 1st line of neighbors around the TSV in center.
- → Inductive coupling is strong → significant coupling extends to the 2nd line of neighbors around the TSV in center.



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A Set of Test Multi-TSV Arrangements

The parasitics of two adjacent TSVs are investigated under different arrangements as shown below





Characteristics of Multi-TSV Structures⁽⁴⁾

Coupling capacitance (a) (b) (c) (d) (e) 35 20 5 10 5 10 15 20 20 5 10 15 20 20 5 10 15 20 20 25TSV Pitch(µm)

coupling capacitance for different TSV arrangements



coupling inductance for different TSV arrangements

Coupling Resistance



coupling resistance for different TSV arrangements

Coupling capacitance and resistance showed clear dependency on TSV arrangement, while coupling inductance remains "almost" constant.



Closed Form Expressions for the Model Elements

Using the dimensional analysis, we developed the following closed form expressions

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TSV Model Validation Against EM Simulation



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Model Usage Time Domain



A comparison of the quasi-static EM simulations against the proposed exact lumped element model simulations for a single TSV



Conclusion

- An equivalent lumped element model of multiple TSVs is introduced,
- Closed-form expressions for the values of the model elements (R, L and C) of different TSVs structures are presented.
- Results showed that:
 - the self resistance and inductance of a TSV are "almost" independent of the TSV arrangement.
 - coupling capacitance and resistance are clearly dependent on the TSV arrangement, while the coupling inductance are "almost" independent of the TSV arrangement.
 - Capacitive and resistive coupling are weak → insignificant beyond the 1st line of neighbors around the TSV in center.
 - Inductive coupling is strong → significant coupling extends to the 2nd line of neighbors around the TSV in center.



QUESTIONS?