

# A Polynomial-Time Custom Instruction Identification Algorithm Based on Dynamic Programming

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# Outline

Introduction

Problem Statement

Algorithm

Experiments and Results

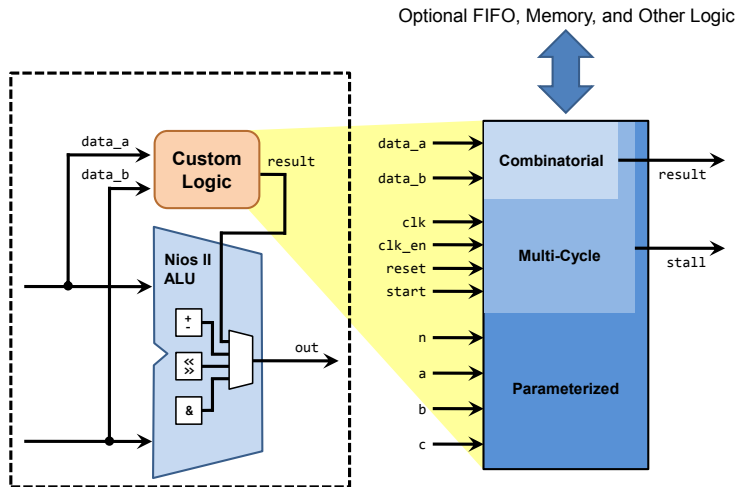
Conclusion

# Configurable Processor

Ready-made general-purpose processor with user-defined instructions for specific applications

- ▶ More flexible and easier to design than ASICs
- ▶ Higher performance and lower power consumption than GPP

# Configurable Processor



Custom Instruction Logic of Nios II Processor

# Configurable Processor

Ready-made general-purpose processor with user-defined instructions for specific applications

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**Problem:** How can we decide what to implement?

# Custom Instruction Identification

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## Best

- ▶ Performance improvement

## Architectural constraints

- ▶ The number of inputs and outputs
- ▶ Types of operations that can be used in the custom functional unit



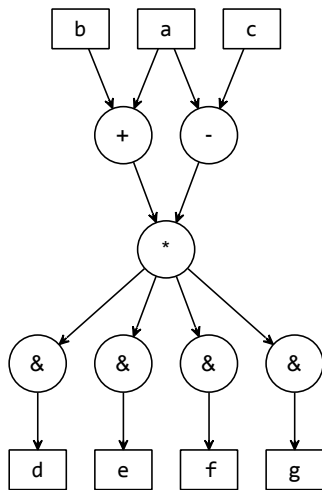
# Custom Instruction Identification

```
b = x + 1;
c = x - 1;
for (a = 0; a < 20; ++a) {
    t = a + b;
    u = a - c;
    d = (t * u) & 0x000f;
    e = (t * u) & 0x00f0;
    f = (t * u) & 0x0f00;
    g = (t * u) & 0xf000;
}
y = d + e + f + g;
```

## CI Identification Flow

1. Select a kernel
2. Convert it into a DFG
3. Find the best CI

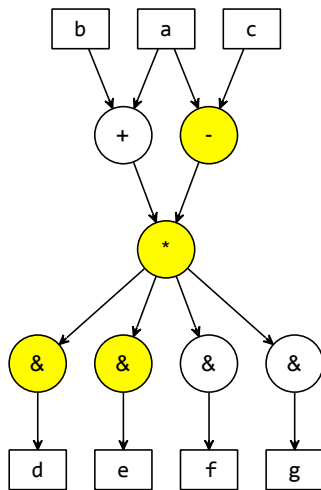
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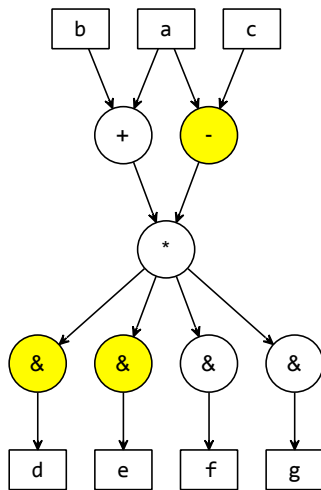
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# Custom Instruction Identification

Formal definition of the problem is as follows:

## Single Custom Instruction Identification

Given a graph  $G$ , find a convex cut  $S$  that maximizes  $M(S)$  under the constraints  $|IN(S)| \leq N_{in}$  and  $|OUT(S)| \leq N_{out}$ .

- ▶  $G$ : a DAG which denotes the data flow of a basic block
- ▶  $S$ : a subgraph of  $G$
- ▶  $IN(S)$ ,  $OUT(S)$ : inputs and outputs of a cut  $S$
- ▶  $M(S)$ : user-defined function for evaluating a cut

## Previous Works

**Optimal solution** with exponential time complexity

- ▶ Branch and bound with pruning (K.Atasu, DAC'03)
- ▶ Integer linear programming (K.Atasu, CODES+ISSS'05)

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- ▶ Too much time is needed for large dataflow graphs.

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**Nonoptimal solution** with lower time complexity

- ▶ Genetic algorithm (L.Pozzi, TCAD'06)
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- ▶ Cone union algorithm (P.Yu, DAC'04)



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## Problems

- ▶ The solution may not be optimal.
- ▶ Only connected cuts can be selected where disconnected cuts give better performance improvement in general.

# Our Approach

Top-down dynamic programming with single cut identification algorithm (branch and bound with pruning)

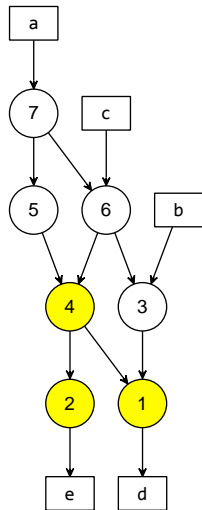
- ▶ Solutions of subproblems are stored in a memoization table
- ▶ The stored solution is used instead of enumeration if available

But, how can we guarantee that constraints are still satisfied even if we use stored solutions?

# Our Approach

## Problem

How can we know that the cut is no longer convex if we add the vertex 7 into the cut?



# Our Approach

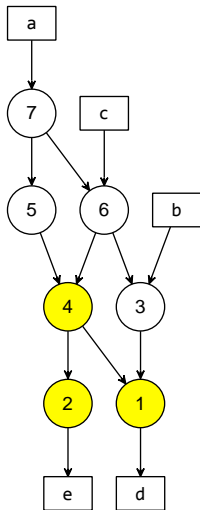
## Problem

How can we know that the cut is no longer convex if we add the vertex 7 into the cut?

## Solution

Convexity constraint is violated when we add a vertex that has a path to an input which

1. is a follower vertex of  $S$ , or
2. has at least one includable ancestor that is a follower vertex of  $S$ .



# Our Approach

## Problem

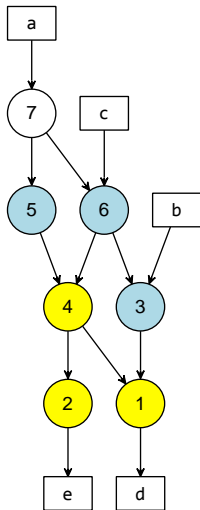
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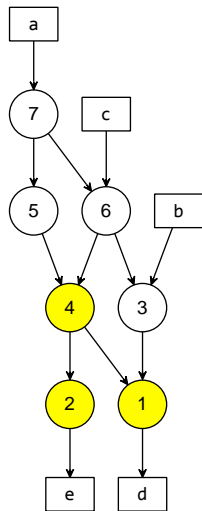
We call such inputs as **watcher inputs**.



# Our Approach

## Problem

How can we calculate  $|IN(S)|$  of the cut  $S' = S \cup \{v\}$  with properties of  $S$ ?



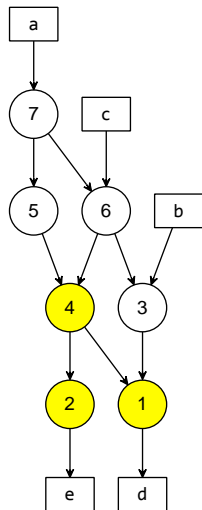
# Our Approach

## Problem

How can we calculate  $|\text{IN}(S)|$  of the cut  $S' = S \cup \{v\}$  with properties of  $S$ ?

## Solution

$$|\text{IN}(S)| = |\text{IN}_w(S)| + |\text{IN}_{nw}(S)|$$

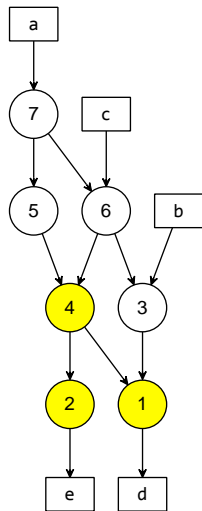




# Our Approach

## Problem

How can we calculate  $|\text{OUT}(S)|$  of the cut  $S' = S \cup \{v\}$  with properties of  $S$ ?



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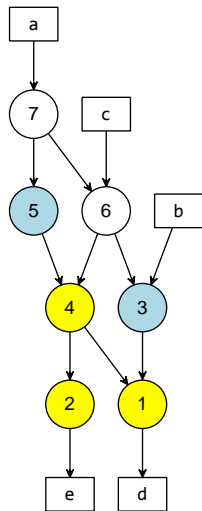
## Problem

How can we calculate  $|\text{OUT}(S)|$  of the cut  $S' = S \cup \{v\}$  with properties of  $S$ ?

## Solution

$v$  is an output if and only if it has at least one outside successor. We call an input with no outside successor as a **dedicated input**. Therefore,

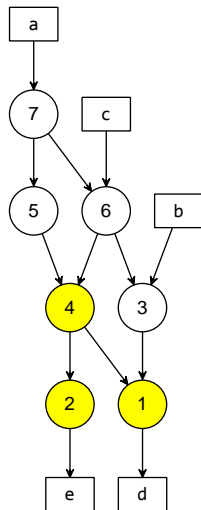
$$|\text{OUT}(S')| = \begin{cases} |\text{OUT}(S)| + 1 & v \notin \text{IN}_d(S) \\ |\text{OUT}(S)| & \text{otherwise} \end{cases}$$



# Our Approach

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How can we calculate dedicated inputs of the cut  $S' = S \cup \{v\}$  with properties of  $S$ ?



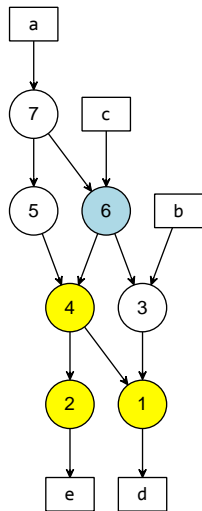
# Our Approach

## Problem

How can we calculate dedicated inputs of the cut  $S' = S \cup \{v\}$  with properties of  $S$ ?

## Solution

When the last successor of an input is added to the cut, the input becomes dedicated unless it already has at least one outside successor (**permanently undedicated inputs**).



# Our Approach

In short, I/O and convexity constraints can be checked with following properties when we inserting a follower vertex  $v$  to the convex cut  $S$ . (a **representative tuple**)

$$t(S) = (IN_w(S), |IN_{nw}(S)|, IN_{pu}(S), IN_d(S), |OUT(S)|, v_{last}(S))$$

Moreover, we found a function  $F$  for the following relation:

$$t(S \cup \{v\}) = F(t(S))$$

# Our Approach

Therefore, we can conclude the following.

## Corollary

*When constructing a bigger convex cut  $S' = S \cup \{v\}$  from a convex cut  $S$  by adding a vertex in the pre-determined traversal order, the constraint on the number of inputs, outputs, and convexity of  $S'$  can be fully determined by only the following properties:  $IN_w(S)$ ,  $|IN_{nw}(S)|$ ,  $IN_{pu}(S)$ ,  $IN_d(S)$ ,  $|OUT(S)|$ , and  $v_{last}(S)$ .*

Thus, we can safely use stored solutions for all cuts with same representative tuple.

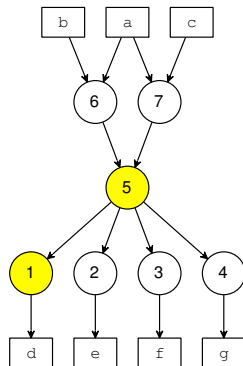
# Our Approach

For  $S_1 = \{1, 5\}$ ,

$$IN_w(S_1) = \{6, 7\} \quad |IN_{nw}(S_1)| = 0$$

$$IN_d(S_1) = \{6, 7\} \quad IN_{pu}(S_1) = \emptyset$$

$$|OUT(S_1)| = 2 \quad v_{last}(S_1) = 5$$



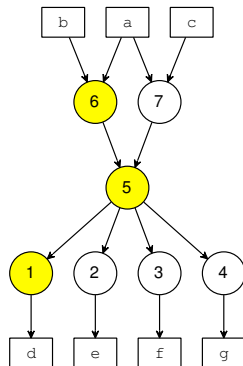
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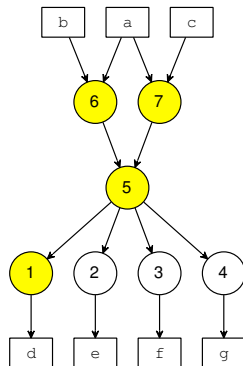
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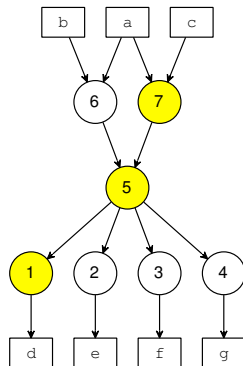
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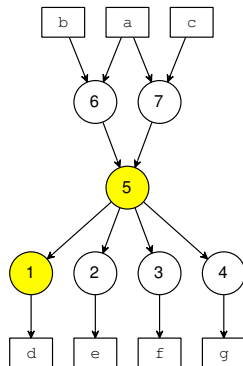
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If the best cut is  $S'_1 = \{1, 5, 6, 7\}$ , store  $S'_1 - S_1$  into the memoization table with a key  $k$ , where

$$k = (IN_w(S_1), |IN_{nw}(S_1)|, IN_d(S_1), \\ IN_{pu}(S_1), |OUT(S_1)|, v_{last}(S_1))$$



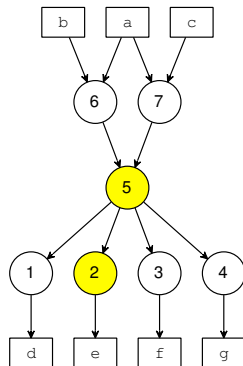
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For  $S_2 = \{2, 5\}$ ,

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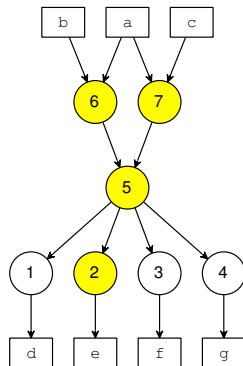
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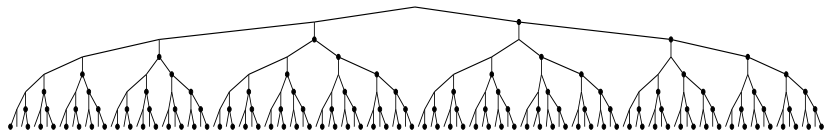
We can use the stored solution from  $S_1$  due to the corollary. Therefore,

$$S'_2 = S_2 \cup \{6, 7\} = \{2, 5, 6, 7\}$$



# Our Approach

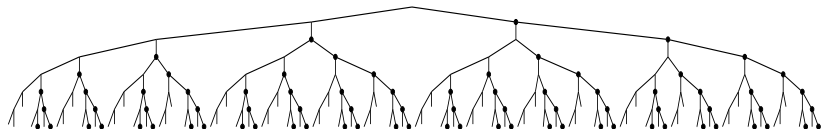
Branch-and-bound search can be visualized as a binary tree.



1. Total search space

# Our Approach

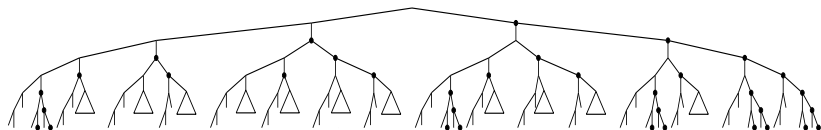
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1. Total search space
2. Pruned by K. Atasu's algorithm (with  $N_{in} = 8$ ,  $N_{out} = 4$ )

# Our Approach

Branch-and-bound search can be visualized as a binary tree.



1. Total search space
2. Pruned by K. Atasu's algorithm (with  $N_{in} = 8$ ,  $N_{out} = 4$ )
3. Used memoized solutions (our approach)



# Our Approach

How about optimality?

## Theorem

*The single cut identification problem has optimal substructure if  $M(S \cup S') = M(S) + M(S')$ .*

Therefore, the algorithm is optimal for  $M(S) = \sum_{v \in S} s_v$ .

However,

$$M(S) = \sum_{v \in S} s_v - \lceil L \rceil$$

is used in general. In this case, the proposed algorithm may give an approximated solution.

# Our Approach

The upper bound for the size of the memoization table:

$$O\left(|V|^{N_{in}} \cdot (N_{in}^{N_{in}})^2 \times N_{in} \times |V|^2 \times N_{out}\right)$$

The time complexity for processing each item:

$$O(N_{in} + |V| + 1)$$

Therefore, the overall time complexity of our algorithm is

$$O\left(|V|^{N_{in}+3} \cdot N_{in}^{3N_{in}} \cdot N_{out}\right)$$

which is polynomial to the number of vertices.

# Our Approach

Therefore, the overall time complexity of our algorithm is

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Note that two previous optimal algorithms have  $O(2^{|V|})$  time complexity.

# Experiments

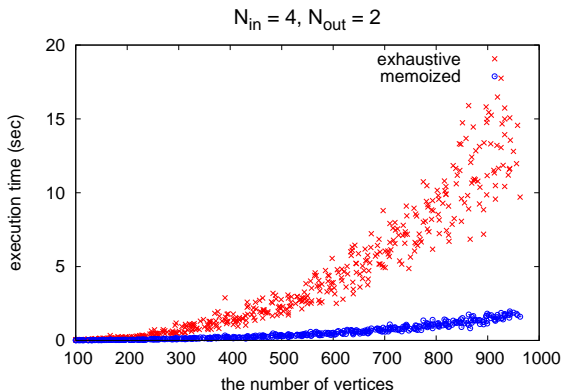
We compared the execution time of the proposed algorithm (**memoized**) to that of the K.Atasu's single cut identification algorithm (**exhaustive**).

## Settings

- ▶ Merit function is defined as a difference between software and hardware latency.
- ▶ The size of the memoization table is limited to one million items. (consumes roughly 500MB memory)

# Synthetic Graphs

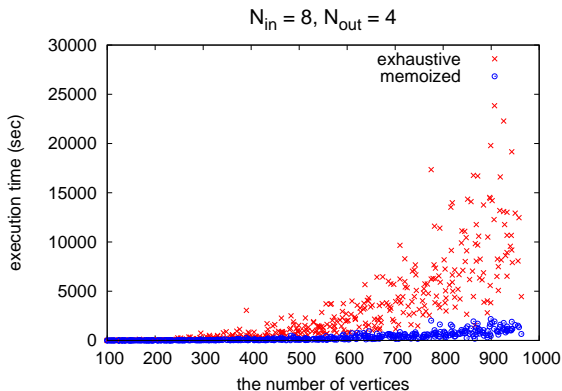
The following result is for randomly generated dataflow graphs with 87 to 963 vertices.



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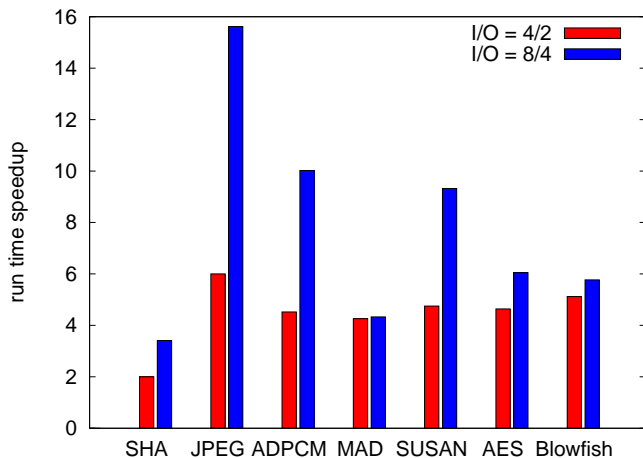
# Real World Applications from MiBench

Name	$ V $	I/O	exhaustive	memoized	Speedup	$d_{SW}^\dagger$	$d_{CI}^\dagger$
SHA	38	4/2	0.032s	0.016s	2.00	50	37
		8/4	3.696s	1.083s	3.41		13
JPEG*	92	4/2	0.120s	0.020s	6.00	164	144
		8/4	69.67s	4.460s	15.62		131
ADPCM*	133	4/2	0.411s	0.091s	4.52	220	193
		8/4	89.71s	8.952s	10.02		152
MAD	137	4/2	1.125s	0.264s	4.26	337	326
		8/4	311.5s	71.91s	4.33		304
SUSAN	197	4/2	0.261s	0.055s	4.75	525	515
		8/4	216.8s	23.26s	9.32		503
AES	247	4/2	1.271s	0.274s	4.64	431	414
		8/4	183.0min	30.24min	6.05		392
Blowfish	414	4/2	1.433s	0.280s	5.12	219	212
		8/4	71.24min	12.35min	5.77		185

\* disconnected graph

$^\dagger$  execution cycle of the basic block without ( $d_{SW}$ ) and with ( $d_{CI}$ ) a custom instruction

# Real World Applications from MiBench





# Conclusion

We proposed a polynomial-time algorithm for custom instruction identification.

- ▶ The correctness of the algorithm is proved theoretically.
- ▶ The algorithm gives an optimal solution for generally used merit function with very high probability.
- ▶ The algorithm is significantly faster than the previous approach.