

# Call for Designs

University LSI Design Contest

ASP-DAC 2011

<http://www.aspdac.com/aspdac2011/>

January 25-28, 2011

Yokohama, Japan



## Aims of the Contest:

As a unique feature of ASP-DAC 2011, the University LSI Design Contest will be held. The aim of the Contest is to encourage education and research on VLSI design at universities and other educational organizations. We solicit designs that fit in one or more of the following categories:

- (1) Designed, and actually implemented on chips in universities or other educational organizations during the last two years;
- (2) Designs that report actual measurements from implementations;
- (3) Innovative design prototypes.

Interesting or excellent designs selected will be honored by providing the opportunities for presentation in a special session at the conference. Award(s) will be given to a few numbers of outstanding designs, selected from those presented at the conference.

## Areas of Design:

Application areas or types of circuits of the original LSI circuit designs include (but are not limited to):

- (1) Analog, RF and Mixed-Signal Circuits, (2) Digital Signal Processing, (3) Microprocessors, (4) Custom ASIC.

Methods or technology used for implementation include:

- (a) Full Custom and Cell-Based LSIs, (b) Gate Arrays, (c) FPGA/PLDs.

## Submission of Design Descriptions:

A camera-ready summary is requested to be prepared within 2 pages including figures, tables, and references. It is strongly recommended that measured experimental results and a chip micrograph are included in the original LSI circuit design. Please do not submit the same paper as a regular paper.

Specification of the submission format will be available at <http://www.aspdac.com/aspdac2011/>

<b>Deadline for summary:</b>	5PM JST (GMT+9) July 19 (Mon.), 2010
<b>Notification of acceptance:</b>	Sep. 24 (Fri.), 2010
<b>Deadline for camera-ready:</b>	5PM JST (GMT+9) Nov. 15 (Mon.), 2010

## Review:

Submitted designs will be reviewed by the Design Contest Committee in a process similar to the review process for the technical papers. The following criteria will be applied in the selection of designs:

- (1) Reliability of design and implementation, (2) Quality of implementation, (3) Performance of the design,
- (4) Novelty of application, algorithm, architecture, (5) Others.

Interesting or excellent designs selected will be presented at a special session of the conference.

## Presentation:

An author of each selected design will be required to make a short presentation at a special session of ASP-DAC 2011. A digest of each design to be presented will be included in the conference proceedings.

Contact Email: [aspdac2011-udc@mls.aspdac.com](mailto:aspdac2011-udc@mls.aspdac.com)

## ASP-DAC 2011 Chairs

<b>General Chair:</b>	<b>Kunihiro Asada (University of Tokyo)</b>
<b>Technical Program Chair:</b>	<b>Hyunchul Shin (Hanyang University)</b>
<b>Design Contest Co-Chairs:</b>	<b>Masanori Hariyama (Tohoku University)</b>
	<b>Hiroshi Kawaguchi (Kobe University)</b>