Call for Participation

ASP-DAC 2015
20th
20th Asia and South Pacific Design Automation Conference

ASIA SOUTH PACIFIC DAC — DESIGN AUTOMATION CONFERENCE

Date: January 19 – 22, 2015
Place: Makuhari Messe International Convention Complex,
Chiba/Tokyo, Japan

Aims of the Conference

ASP-DAC is the largest conference in Asia and South-Pacific regions on Electronic Design Automation (EDA) area for VLSI and systems. ASP-DAC has been started at 1995 and this ASP-DAC 2015 is 20th conference. ASP-DAC 2015 offers you an ideal opportunity to touch the recent technologies and the future directions on the LSI design and design automation areas by technical papers and tutorials. ASP-DAC also holds Designers' Forum to make presentations about the latest designs for designers. Please do not miss ASP-DAC 2015.

Features of ASP-DAC 2015

■ Keynote Speeches

1. Udo Wolz, Executive Vice President of Bosch, "The required technologies for Automotive towards 2020"
2. Atsushi Takahara, Director of NTT Network Innovation Laboratories, "Programmable Network"
3. Noriko Arai, Professor of National Institute of Informatics, "When and how will an AI be smart enough to design?"

■ Tutorials

ASP-DAC 2015 will hold the following six tutorial lectures. Audience can take any combination of 3 lectures.

"FD-SOI"   by Andreia Cathelin (STMicroelectronics)

"Leading-Edge Lithography and TCAD"   by Shigeki Nojima (Toshiba)

"Normally-Off Computing: Synergy of New Non-Volatile Memories and Aggressive Power Management"
        by Takashi Nakada (Univ. of Tokyo) & Hiroshi Nakamura (Univ. of Tokyo)

"Hardware Trust in VLSI Design and Implementations"
        by Kazuo Sakiyama (Univ. of Electro-Communications) & Makoto Nagata (Kobe Univ.)

"High-Level Synthesis for FPGAs: From Software to Programmable Hardware" by Jason Anderson (Univ. of Toronto)

"Electronic Design Automation for Nanotechnologies"
        by Pierre-Emmanuel Gaillardon (EPFL) & Giovanni De Micheli (EPFL)

■ Designers' Forum

Designers' Forum is conceived as a unique program that shares the design experience and solutions of real product developments among LSI designers and EDA academia/developers. The topics discussed in this forum include the next generation car electronics, 4K/8K TV technologies, data-centric computing platform, IP-based SoC design and IP design innovations.

■ University LSI Design Contest

In University LSI Design Contest, state-of-the-art LSI designs compete on their design excellence and implementation quality. More than 20 high-quality designs all including actual silicon proof will be introduced at the short presentation and poster sessions.

■ Technical Sessions

There are 106 high quality papers selected from 318 submissions. We also plan the following special sessions:
"Internet of Things", "The Future of Emerging ReRAM Technology", "Machine Learning in EDA: Promises and Challenges in Selected Applications", "New Challenges and Solutions in Nanometer Physical Design", and "System-Level Designs and Tools for Multicore Systems"

Sponsors

ACM SIGDA, IEEE CASS, IEEE CEDA, IEICE ESS, IPSJ SIG-SLDM
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Conference Secretariat

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