ASP-DAC 2016 Conference Program at A Glance (Final)

25 Jan 2016 (Mon)

Time	Tufan (TF) Room No.			
	TF 4303	TF 4203	TF 4304	TF 4204
07:30~17:00	On-Site Registration *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*			
09:00~11:30			12th International Workshop on Compact Modeling (IWCM) Program	
09:00~12:00	**Tutorial 1: Machine Learning and Neuromorphic Computing Acceleration Organizer:** Yiran Chen (University of Pittsburgh) **Speakers:** Yu Wang (Tsinghua University) & Yiran Chen (University of Pittsburgh)	**Tutorial 2: Directed Self-Assembly Lithography (DSAL): Mask Synthesis and Circuit Design Organizer:** Youngsoo Shin (KAIST) **Speakers:** Youngsoo Shin (KAIST) & Seongbo Shim (Samsung)		
14:00~16:30			12th International Workshop on Compact Modeling (IWCM) Program	
14:00~17:00	**Tutorial 3: Uncertainty Quantification for Electronic Systems: State of the Art and Recent Progress Organizer:** Luca Daniel (MIT) **Speakers:** Luca Daniel (MIT) & Zheng Zhang (MIT, and Argonne National Labs)	**Tutorial 4: Energy-efficient Data Analytics: Thousand-core Accelerator In-Memory with Reconfigurable I/Os Organizer & Speaker:** Hao Yu (Nanyang Technological University)		
18:00~20:00	Welcome Reception *(Location: N1-G008, University of Macau)*			

26 Jan 2016 (Tue)

Time	Tufan (TF) Room No.															
	TF 4303				TF 4203				TF 4304				TF 4204			
07:30~17:00	On-Site Registration *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*															
08:30~09:00	Opening Ceremony *(Location: TF 4201A-4302, Level 4, Holiday Inn, Sands Cotai Central)*															
09:00~10:00	Keynote Addresses I--"The Next Decade", Alessandro Cremonesi, Group Vice President, General Manager of ST Central Labs at STMicroelectronics *(Location: TF 4201A-4302, Level 4, Holiday Inn, Sands Cotai Central)*															
10:00 ~ 10:20	Coffee Break *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*															
10:20~12:00	1S. University Design Contest				1A. The Optimization of Memory Architecture and Management				1B. Secure Embedded Systems & IoT				1C. Design for Directed Self-Assembly			
	2001	2002	2003	2005	1009	1022	1185	1247	1017	1112	1186	1222	1027	1068	1098	1179
	2006	2007	2008	2009												
	2010	2011	2012	2013												

Time																
12:00~13:30	**Lunch/Poster Discussion** *(Location: TF 4206-4307, Holiday Inn, Sands Cotai Central)*															
13:50~15:30	2S (Special Session) Designing with Spintronics: Recent Developments and Upcoming Challenges				2A. Advances in Verification				2B. System Simulation and Testing				2C. Advanced Issues in Floorplanning and Placement			
	1304	1305	1306		1047	1164	1215	1261	1091	1139	1231	1248	1104	1143	1233	1243
15:30~15:50	**Coffee Break** *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*															
15:50~17:30	3S (Special Session) High-Level Synthesis – Now, the Future, and the "Dark Secrets"				3A. Robust Timing Analysis and Optimization				3B. Low Power in Deep Sub-Micro: From Architecture to Physical Design				3C. Emerging Devices for Energy Efficient Computing			
	1308	1309	1310	1311	1130	1131	1132	1252	1120	1136	1182	1275	1086	1148	1184	1211
17:45~19:45	**ACM SIGDA Student Research Forum** *(Location: TF 4201A-4302, Level 4, Holiday Inn, Sands Cotai Central)*															

27 Jan 2016 (Wed)

Time	Tufan (TF) Room No.			
	TF 4303	TF 4203	TF 4304	TF 4204
07:30~17:00	**On-Site Registration** *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*			
09:00~10:00	**Keynote Addresses II--"Systems of Systems - The Next Frontier of Semiconductor", Qi Wang, VP and Chief of Staff to the CEO at Cadence Design Systems, Inc.** *(Location: TF 4201A-4302, Level 4, Holiday Inn, Sands Cotai Central)*			
10:00~10:20	**Coffee Break** *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*			

Time																
10:20~12:00	4S (Special Session) Design Challenges for Energy-Efficient IoT Edge Devices				4A. Taking Advantages of Uncertainty in System Optimization				4B. Security and Reliability in Emerging Devices				4C. Routing			
	1325	1326	1327	1334	1073	1138	1153	1259	1019	1020	1236	1242	1090	1096	1107	1189
12:00~13:30	**Lunch** *(Location: TF 4206-4307, Holiday Inn, Sands Cotai Central)*															
13:50~15:55	5S (Special Session) Cross-Layer Resilience: Snapshots from the Frontier of Design				5A (Special Session) Design Automation of Energy-Efficient Smart Buildings and Smart Cars				5B. Advanced Embedded Software Techniques: Sensing, Computation, and Storage				5C. Advances in Logic Synthesis			
	1312	1313	1314	1315	1316	1317	1318	1319	1056	1057	1192	1196	1029	1081	1084	1166
									1200				1260			
15:55~16:15	**Coffee Break** *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*															
19:00 - 21:30	**Banquet** *(Location: Convention and Exhibition Center, Macau Fisherman's Wharf)*															

28 Jan 2016 (Thu)

Time	Tufan (TF) Room No.			
Time				

Time	TF 4303	TF 4203	TF 4304	TF 4204
07:30~17:00	**On-Site Registration** *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*			
08:30~10:00	**Joint-Keynote III--"Majority-based Synthesis for Nanotechnologies", Giovanni De Micheli, Professor, École Polytechnique Fédérale de Lausanne, Lausanne, Switzerland** **Joint-Keynote III--"A Scalable Communication-Aware Compilation Flow for Programmable Accelerators, Jason Cong, Chancellor's Professor, UCLA Computer Science Department Director, Center for Domain-Specific Computing** **Joint-Keynote III--"Software and System Co-optimization in the era of Heterogeneous Computing", Ruchir Puri, IBM Fellow, IBM Thomas J Watson Research Center, USA** *(Location: TF 4201A-4302, Level 4, Holiday Inn, Sands Cotai Central)*			
10:00 ~ 10:20	**Coffee Break** *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*			
10:20~12:00	6S (Special Session) Cyber-Physical Systems and Security	6A. Testing, Modeling and Optimization Techniques for Analog Circuits	6B. Energy-Efficient & Customized Computing	6C. Design Methodologies for Microfluidic Biochips
	1320 / 1321 / 1322 / 1323	1012 / 1097 / 1193 / 1265	1108 / 1147 / 1198 / 1271	1023 / 1050 / 1083 / 1111
12:00~13:30	**Lunch** *(Location: TF 4206-4307, Holiday Inn, Sands Cotai Central)*			
13:50~15:30	7S (Special Session) New Frontiers of Physical Design	7A. System-Level Design for Energy-Efficiency and Reliability	7B. Design for Trustworthy IC	7C. Design for Reliability
	1300 / 1301 / 1302	1045 / 1046 / 1074 / 1101	1053 / 1150 / 1209 / 1228	1106 / 1224 / 1227 / 1299
15:30~15:50	**Coffee Break** *(Location: Foyer, Level 4, Holiday Inn, Sands Cotai Central)*			
15:50~17:30	8S (Special Session) Reliability, Adaptability and Flexibility in Timing	8A. Emerging Networks-on-Chip Designs	8B. Test and Debug	8C. Emerging Devices and Systems for Cyber-Physical Applications
	1303	1025 / 1089 / 1093 / 1128	1121 / 1126 / 1181 / 1286	1069 / 1127 / 1256 / 1257
14:00	**Macau Heritage Half Day Tour**			

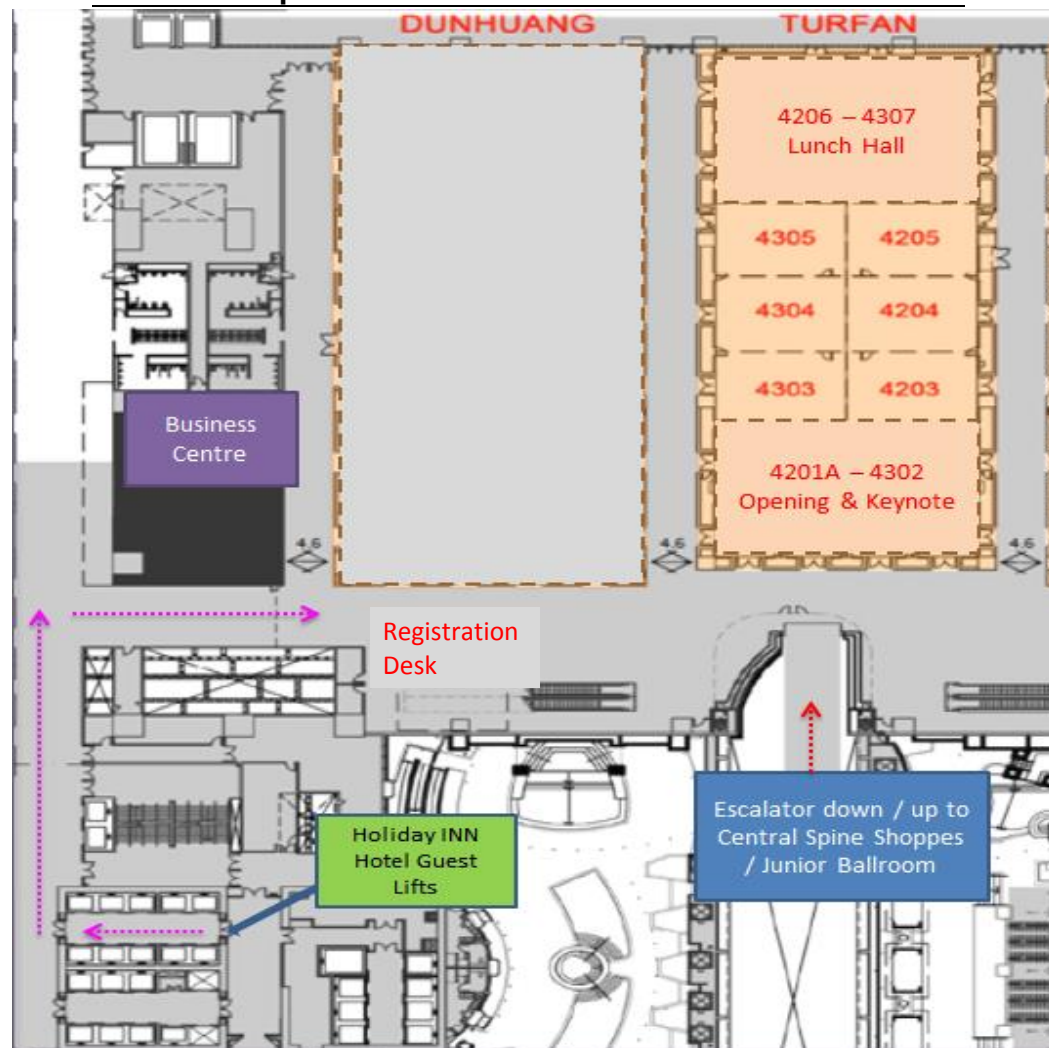## Room Assignment

Room	Event
Foyer (4F)	Registration and Information Desk

TF 4201A-4302 (4F)	Opening, Keynote I & II, Joint Keynote III, and ACM SIGDA Student Research Forum
TF 4206-4307 (4F)	Lunch and Poster Discussion
TF 4303 (4F)	Tutorial 1 & 3 and Session 1S, 2S, 3S, 4S, 5S, 6S, 7S & 8S
TF 4203 (4F)	Tutorial 2 & 4 and Session 1A, 2A, 3A, 4A, 5A, 6A, 7A & 8A
TF 4304 (4F)	IWCM Program and Session 1B, 2B, 3B, 4B, 5B, 6B, 7B & 8B
TF 4204 (4F)	Session 1C, 2C, 3C, 4C, 5C, 6C, 7C & 8C
TF 4205 (4F)	Rehearsal
TF 4305 (4F)	Exhibition
N1-G008, University of Macau	Welcome Reception
Convention and Exhibition Center, Macau Fisherman's Wharf	Banquet

Instruction to the Venue

Venue Floorplan

ASP-DAC Welcome Reception

18:00 - 20:00, 25 January 2016 (Monday)

At N1-G008, University of Macau (Departure at Hotel Lobby, Holiday Inn, Sands Cotai Central @ 17:30)

Welcome Reception: N1-G008, University of Macau

ASP-DAC Banquet

19:00 - 21:30, 27 January 2016 (Wednesday)

Convention and Exhibition Center, Macau Fisherman's Wharf

Convention and Exhibition Center, Macau Fisherman's Wharf