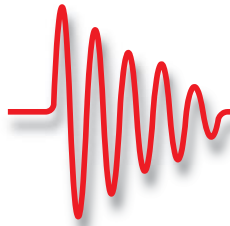


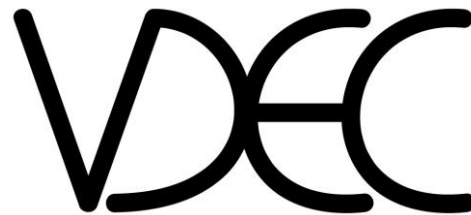
1S-13 CMOS-on-Quartz Pulse Generator for Low Power Applications



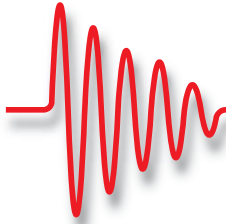
Parit Kanjanavirojkul¹, Nguyen Ngoc Mai-Khanh²,
Tetsuya Iizuka^{1,2}, Toru Nakura^{1,2}, and Kunihiro Asada^{1,2}

¹Graduate School of Engineering, The University of Tokyo, Japan

²VLSI Design and Education Center (VDEC), The University of Tokyo, Japan



1S-13 CMOS-on-Quartz Pulse Generator for Low Power Applications



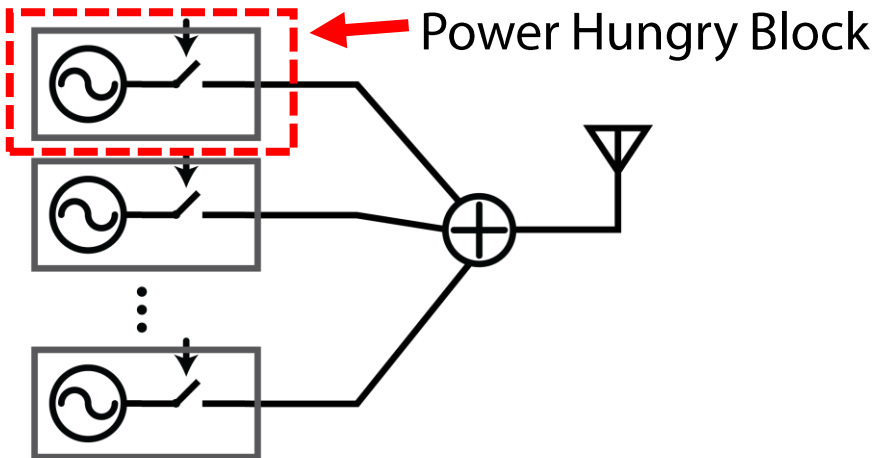
Ultra-Wideband Impulse-Radio

Applications

- Radar Range Finder
- Chemical/biological spectroscopy

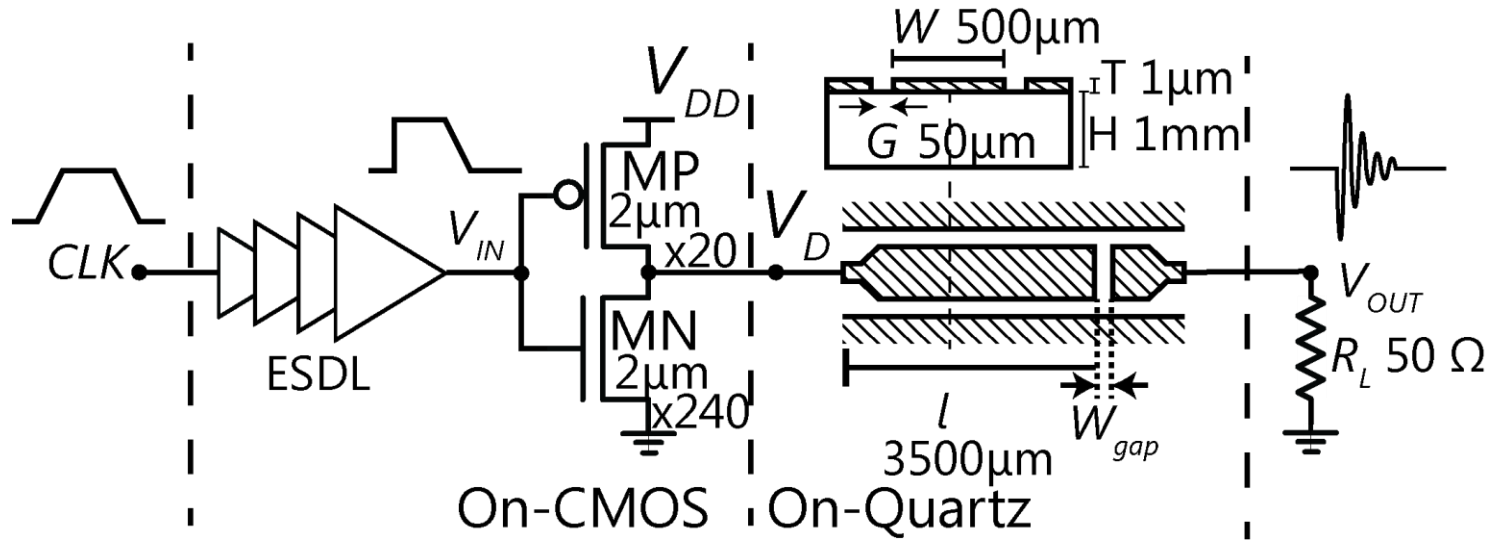
Low Power	Low Duty Cycle	High Frequency
High Efficiency	Zero stand by power Quick Start	CMOS limitation

Pulse Generator

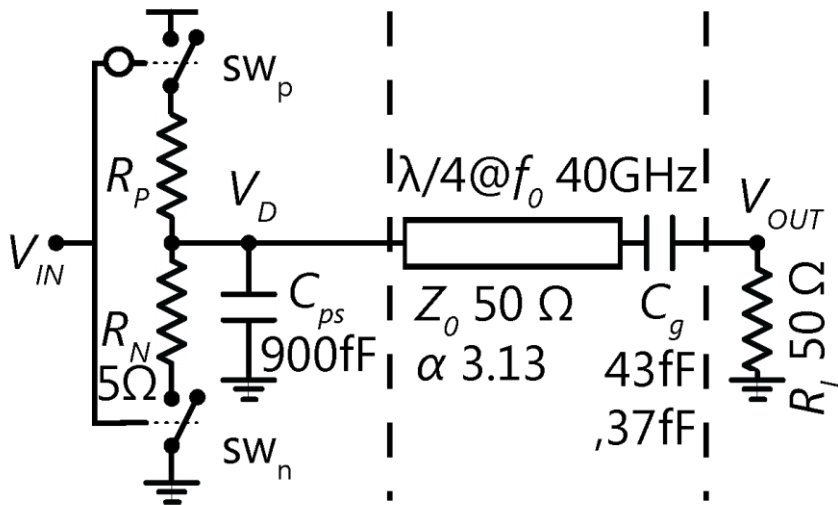


- Conventional Methods fails
- Pulsed Oscillator
 - Edge Combiner
 - Lump Charge-Discharge

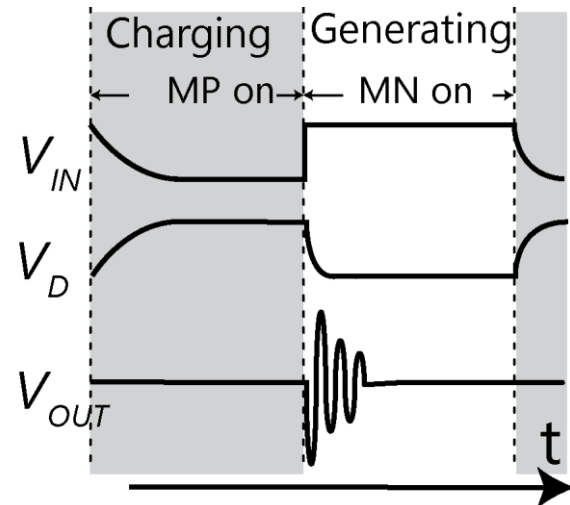
Proposed Circuit

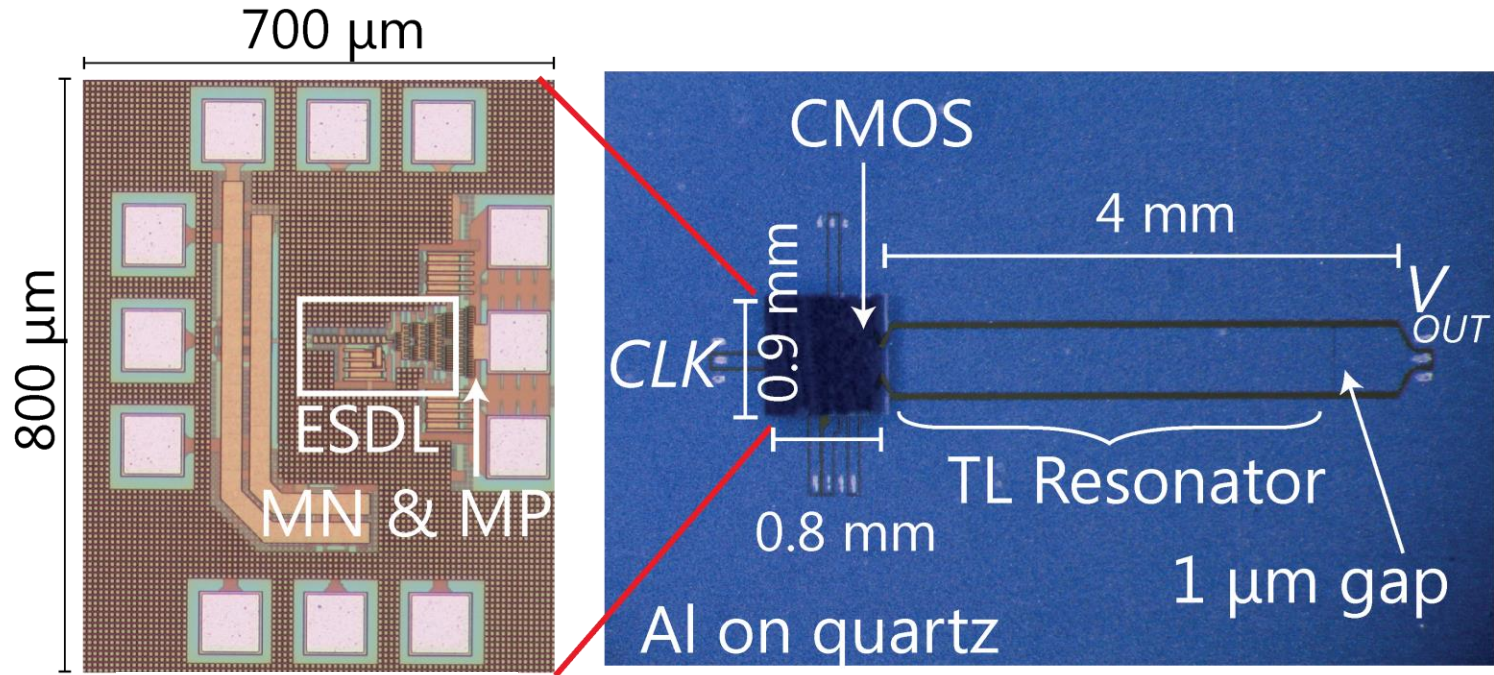
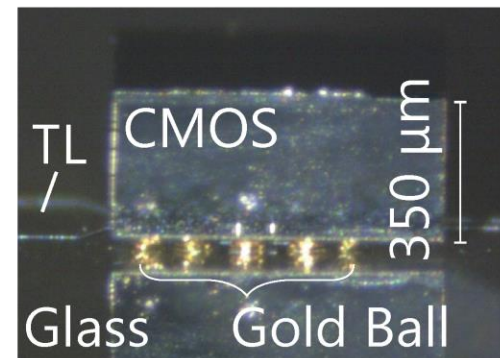
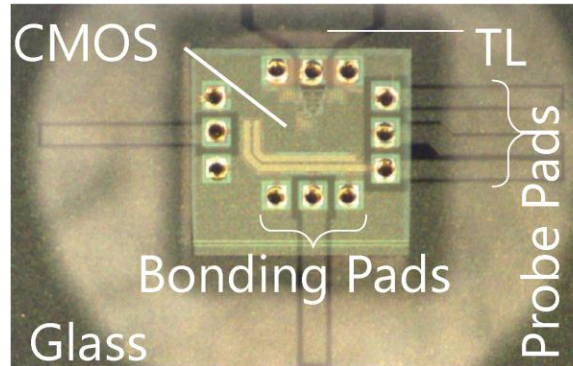
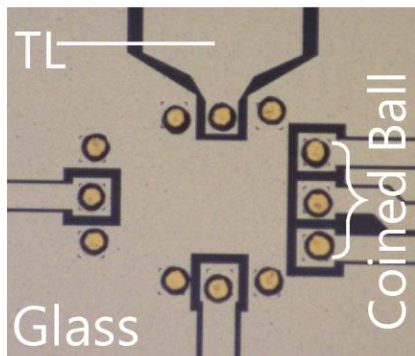


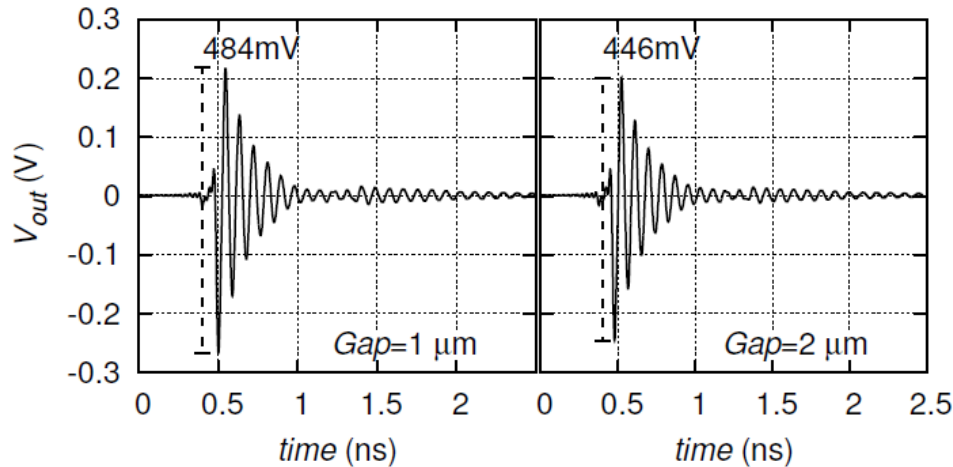
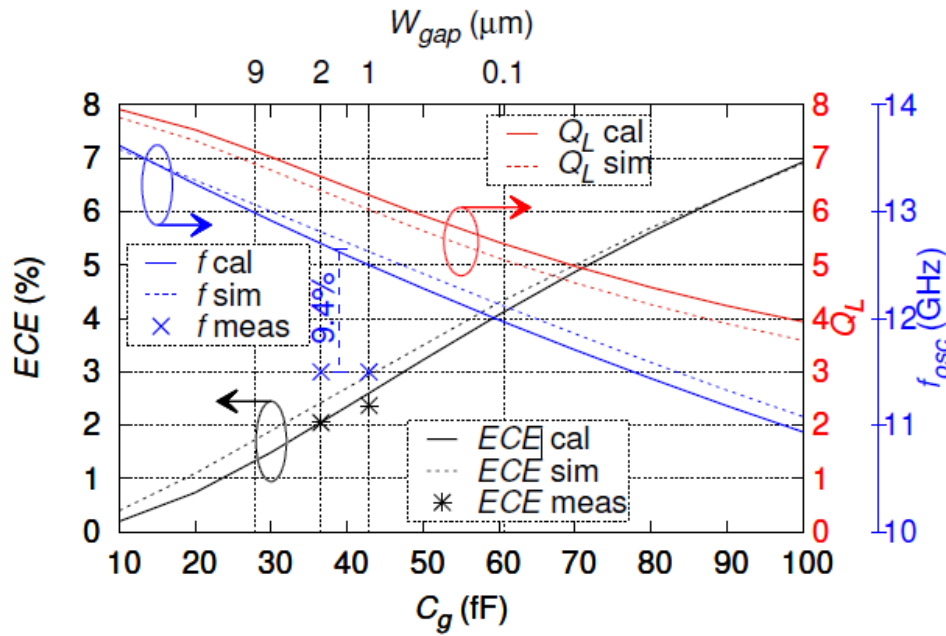
Simplified Circuit



Timing Diagram



*CMOS Chip**Integrated Pulse Generator**Flipped chip integration*

Time Domain Waveform**Output Characteristics****Performance Summary**

Frequency	11.5 GHz
Efficiency	2.37 %
Output Energy	128 fJ/pulse
Input Energy	5.4 pJ/pulse
Repetition Rate	200 Mb/s