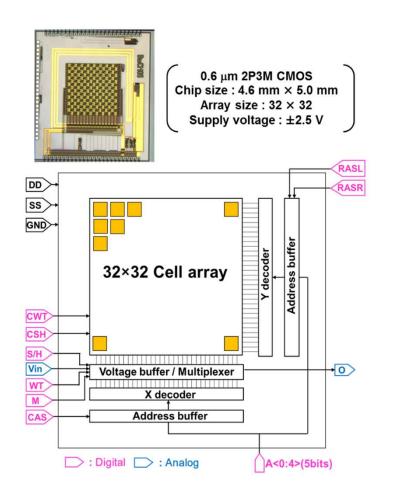
A Low-Voltage CMOS Electrophoresis IC Using Electroless Gold Plating for Small-Form-Factor Biomolecule Manipulation

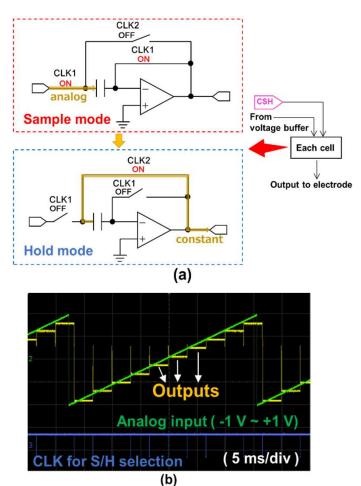
Kiichi Niitsu, Yuuki Yamaji, Atsuki Kobayashi, Kazuo Nakazato (Nagoya University, Japan)

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Background & Research object

■ Low-voltage (sub-1V) CMOS Electrophoresis



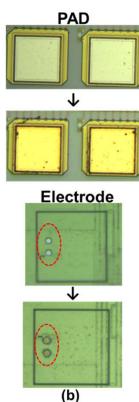


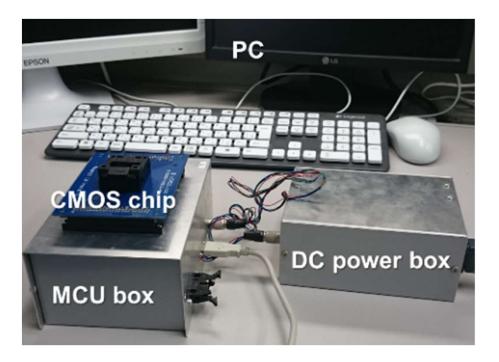
Electrode Design

■ Electrode & System

- 1. Pre-cleaning Acetone→IPA→DI water 25 °C, each 2 min.
- 2. De-oxidizing in aluminum etchant 10 % NaOH, 25 °C, 10 s.
- 3. Zincation 20 % HNO $_3$, 25 °C, 30 s. 40 °C, 45 s. 20 % HNO $_3$, 25 °C, 30 s. 40 °C, 30 s.
- 4. Electroless nickel plating 90 °C, 5 min.
- 5. Electroless gold plating 90 °C, 20 min.

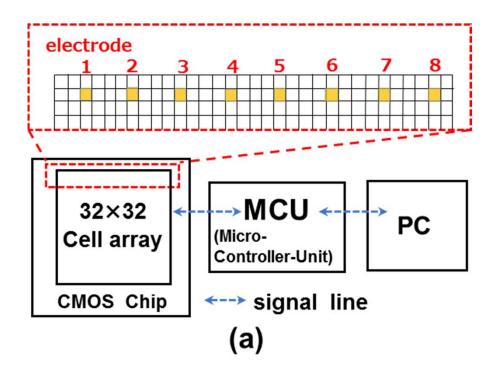
(a)

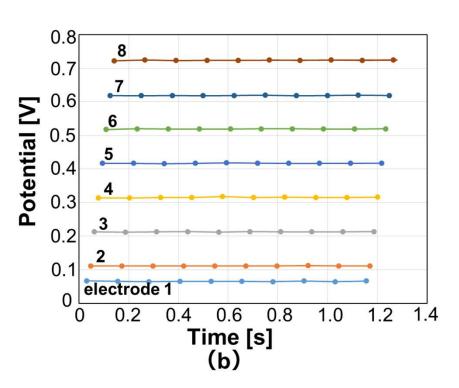




Testchip Design & Function Test

■ Function has been verified





Measurement Results

■ Successfully verified

