

Call for Designs

University LSI Design Contest

ASP-DAC 2022

<http://www.aspdac.com/>

January 17, 2022 – January 20, 2022

Virtual Conference



Aims of the Contest:

As a unique feature of ASP-DAC 2022, the University LSI Design Contest will be held. The aim of the Contest is to encourage education and research on VLSI design at universities and other educational organizations. We solicit designs that fit in one or more of the following categories:

- (1) Designed, and actually implemented on chips in universities or other educational organizations during the last two years;
- (2) Designs that report actual measurements from implementations;
- (3) Innovative design prototypes.

Interesting or excellent designs selected will be honored by providing the opportunities for presentation in a special session at the conference. Award(s) will be given to a few numbers of outstanding designs, selected from those presented at the conference.

Areas of Design:

Application areas or types of circuits of the original LSI circuit designs include (but are not limited to):

- (1) Analog, RF and Mixed-Signal Circuits, (2) Digital Signal Processing, (3) Microprocessors, (4) Custom ASIC.

Methods or technology used for implementation include:

- (a) Custom ASIC and Cell-Based LSIs, (b) Gate Arrays, (c) FPGA/PLDs.

Submission of Design Descriptions:

A camera-ready summary is requested to be prepared within 2 pages including figures, tables, and references. It is strongly recommended that measured experimental results and a chip micrograph are included in the summary.

Specification of the submission format will be available at <http://www.aspdac.com/>

Deadline for abstract:	5 PM AOE (Anywhere on earth)	July 21 (Wed), 2021
Deadline for summary:	5 PM AOE (Anywhere on earth)	July 28 (Wed), 2021
Notification of acceptance:		Sep. 13 (Mon), 2021
Deadline for camera-ready:	5 PM AOE (Anywhere on earth)	Nov. 5 (Fri), 2021

Please note that each paper shall be accompanied by at least one different conference registration at the speaker's registration rate (e.g., two speaker registrations are needed for presenting two accepted papers). But any registered co-author can present the work at the conference. ACM and IEEE reserve the right to exclude a paper from distribution after the conference (e.g., removal from ACM Digital Library and IEEE Xplore) if the paper is not presented at the conference by the author of the paper. Please do not submit the same paper as a regular paper.

Review:

Submitted designs will be reviewed by the Design Contest Committee in a process similar to the review process for the technical papers. The following criteria will be applied in the selection of designs:

- (1) Reliability of design and implementation, (2) Quality of implementation, (3) Performance of the design, (4) Novelty of application, algorithm, architecture, (5) Others.

Interesting or excellent designs selected will be presented at a special session of the conference.

Presentation:

An author of each selected design will be required to make a short presentation at a special session of ASP-DAC 2022. A digest of each design to be presented will be included in the conference proceedings. **In light of the continued Covid-19 pandemic situation, the format of the conference is subject to future change. The options include fully in-person conference, hybrid conference (in person plus live video presentation) and fully virtual conferences. All papers presented, whether in-person or online, will be published. Please check the ASP-DAC 2022 website for updates.**

ASP-DAC 2022 Chairs General Chair:
 Technical Program Chair:
 Design Contest Co-Chairs:

Ting-Chi Wang (National Tsing Hua University, Taiwan)
Masanori Hashimoto (Osaka University, Japan)
Ing-Chao Lin (National Cheng Kung University, Taiwan)
Tsung-Te Liu (National Taiwan University, Taiwan)

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