## WAL: A Novel <u>Waveform Analysis Language</u> for Advanced Design Understanding and Debugging



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### Bio

- Lucas Klemmer
- PhD student at the Institute for Complex Systems
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- Interests:
  - Verification
  - $^{\circ}\,$  EDA tools
  - ° RISC-V





## **Waveform Debugging: Problem or Solution?**

- Waveforms are the fundamental data format for HW development
   Produced by simulators and formal tools
- Proven technique for design understanding and debugging
- Waveforms contain incredible amounts of information • performance, correctness, data/control flow, optimization, ...
- Problems
  - ° 100% manual process
  - Tedious and slow navigation
  - ° Only small slice of data visible at once
  - Only for "simple" signal relations
  - ° Cannot be automated



### **WAL: Waveform Analysis Language**

- WAL is *Domain Specific Language* (DSL) to express HW analysis problems
- Specialized language constructs for HW domain:
  - Waveform signals
  - $^{\circ}$  Time
  - Hierarchy (modules, submodules)
  - Signal relations (bus interfaces)
- Not just true/false expressions, much more than SVA, PSL, ...
- Full capabilities of scripting languages (functions, external libraries, ...)
- Implemented in Python
  - Access to a billion Python packages!



## **WAL Expressions**

- Inspired by LISPs S-Expressions
- WAL programs consist of expressions
  - ° Constants: 1, "hello, world!"
    - 1 ⇒ 1
  - ° Symbols: var1, var2
    - var1  $\Rightarrow$  e.g. 5
  - Lists: (1 2 3)
  - Operators: (+ 1 2 var1)  $\Rightarrow$  8
  - User functions: (defun foo [bar] ...) (foo 5)
  - Waveform signals: Top.module.data\_o  $\Rightarrow$ ?



### **Accessing Waveform Data**

- What is the result of evaluating a signal?
- Depending on:
  - Loaded waveform
  - ° Timepoint in the waveform
- Move index using (step) function

(step 6) 6: comp1.ack  $\Rightarrow$  0 (step 2) 8: comp1.ack  $\Rightarrow$  1





#### **Timed Signal Access**

• Time index can be locally modified with expr@offset syntax

- Example: signal@1 evaluated at INDEX + 1
- o Signal value change: (!= signal@-1 signal)
- Rising clock edge: (&& (! clk@-1) clk)
- @ can be applied to every expression (not just signals)
  Is data larger than 5 two indices ahead?: (> data 5)@2



## Example: Avg. Delay (1)

- Calculate avg. delay on handshaking bus
- Two states:
  - o Waiting: (&& req (! ack))
  - o Sending: (&& req ack)
- Count states
- Result = |waiting| / |sending|







# Example: Avg. Delay (2)

- Calculate avg. delay on handshaking bus
- Two states:
  - o Waiting: (&& req (! ack))
  - o Sending: (&& req ack)
- Count states
- Result = |waiting| / |sending|

(whenever clk
 (when (&& req (! ack)) (inc wait))
 (when (&& req ack) (inc packets)))
(print (/ wait packets))



(3+2+1+2) / 4 = 8/4 = 2



## Example: Avg. Delay (3)

- HW designs ideal for writing generic code!
- Generic expressions with groups
- Expression evaluated in each group
- #signal expanded to full name

(groups "req" "ack")  $\Rightarrow$  (comp1. comp2.)



(print (/ wait packets))



 $((3+2+1+2) + (4+2+1)) / 7 = (8 + 7) / 7 = 15/7 \approx 2.1$ 



## **Other WAL Features**

- Data Structures
  - Lists:
    - (first list), (second list), (rest list), ...
    - list[i], list[h:1]
    - Fold, fold/signal , map, for ...
  - Hashmaps:
    - (geta symbol key1 key2 ...)
    - (seta symbol key1 key2 ... data)
- Extracting bits from signals
   signal[i], signal[h:1]
- Finding indices at which condition is true
   (find cond) => (40 56 102 ...)
- WAL as a compilation target from other languages

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#### **Example Applications (1)**

#### Basic block/CFG extraction



#### **VexRiscv Pipeline Visualization**

318	lw x3, 0(x1)	addi x2, x2, 40	auipc x2, 1	addi x1, x1, 0
320	addi x4, x3, 1	lw x3, 0(x1)	addi x2, x2, 40	auipc x2, 1
322	addi x4, x3, 1	addi x4, x3, 1	lw x3, 0(x1)	addi x2, x2, 40 👘
324	addi x4, x3, 1	addi x4, x3, 1	addi x4, x3, 1	lw x3, 0(x1)
326	flush	flush	flush	flush

Warning 1: Pipeline flushed, current instruction lw x3, 0(x1) Previous Next

328	addi x4, x3, 1	addi x4, x3, 1	addi x4, x3, 1	lw x3, 0(x1)
330	addi x5, x3, 2047	addi x4, x3, 1	addi x4, x3, 1	addi x4, x3, 1
332	lw x3, 0(x1)	addi x5, x3, 2047	addi x4, x3, 1	addi x4, x3, 1
334	addi x4, x3, 1	lw x3, 0(x1)	addi x5, x3, 2047	addi x4, x3, 1
336	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	addi x5, x3, 2047
338	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
340	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
342	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
344	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
346	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
348	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
350	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
352	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)	lw x3, 0(x1)
354	addi x4, x3, 1	addi x4, x3, 1	lw x3, 0(x1)	lw x3, 0(x1)

Warning 1: Pipeline halted for 11 cycles, current instruction lw x3, 0(x1) <u>Previous Next</u>

356	addi x4, x3, 1	addi x4, x3, 1	addi x4, x3, 1	lw x3, 0(x1)
250	addi == = 2 2047	addim/ m2 1	addi ++ 1 ++ 2 1	add:12 1



### **Example Applications (2)**

#### Interactive Debugging REPL

uart ) wal
WAL 0.6.0
>-> (load "uart.vcd" uart)
uart >-> (alias clk uart_tx_test.clk)
uart >-> (alias state uart_tx_test.test_uart.uart_rx.sta
te)
uart >-> (find (&& clk (= state@-2 0) (= state 1)))
(1737 11313 20889 30465 40041 49617 59137 68713 78289 87
865 97441 107017 116593 126169 135745 145265)
uart >-> (step 1737)
#t
uart >-> state
1
uart >-> uart_tx_test.test_uart.uart_rx.data
0
uart >->

#### Throughput, Latency on AXI bus





## Conclusion

- Waveform viewing is highly manual
- WAL enables programmable waveform analysis
  - Data aggregation
  - Data visualization
  - ° Complex queries
- WAL availability
  - GitHub: <u>https://github.com/ics-jku/wal</u>
  - ° Soon also on PyPi

Essential Operations		
Function	Syntax	Semantic
load	(load file id)	Load waveform from file as id
step	(step id amount)	Step trace $id$ by amount (arguments are optional, default $id = all, amount = 1$ )
time information	INDEX	Returns the current time index (starting at $0, 1,$ )
	TS	Returns the current time as specified in the waveform
		Depends on when data was dumped during simulation. Not necessarily continues.
signal information	SIGNALS	Returns a list of all signals in the waveform
scope information	SCOPES	Returns a list of all scopes
signal renaming	(alias name signal)	Introduces an alias name for signal
signal renaming	(unalias name)	Removes alias name
signal, variable access	symbol	If symbol is a signal return signal value at current index
		If symbol is a bound variable return value of variable
		otherwise bind symbol to 0 and return 0
signal bit access	(slice expr index)	Evaluates expr and returns the bit at position index
signal slicing	(slice expr upper lower)	Evaluates expr and returns the slice of bits specified by the upper and lower argumer
relative evaluation	(reval expr offset)	evaluate $expr$ at current index + offset
Advanced Operations		
group detection	(group p <sub>0</sub> p <sub>1</sub> p <sub>n</sub> )	returns a list of all partial signal names $g$ for which $g + p_n$ is an existing signal
group capturing	(in-group g expr)	Captures the group $g$ and then evaluates $expr$ .
	(in-groups g expr)	Captures all groups from the list $g$ and then evaluates $expr$ for each of these groups.
group resolution	(resolve-group p)	Takes the current group $g$ and appends $p$ . If $g + p$ is an existing signal return value
scope capturing	(in-scope s expr)	Captures the scope s and then evaluates expr.
1	(in-scopes s expr)	Captures all scopes from the list $s$ and then evaluates $expr$ for each of these scope.
scope resolution	(resolve-scope p)	Takes the current scope s and appends p. If $s + p$ is an existing signal return value
current group	CG CG	Returns the currently captured group Returns the currently captured group
current cope	CS	Returns the currently captured scope Returns a list of all cooper
scope mormation	SCOPES	Returns a list of an scopes
Conditional Stepped Evaluation	(find cond)	Returns a list of all time indices where cond evaluates to true
Showthand Syntax	(whenever cond expr)	Steps through the waveform and executes expr when cond evaluates to true
Shorthand Syntax		
relative evaluation	expr@sint	(reval expr sint)
relative evaluation list	exprw(sint <sub>0</sub> sint <sub>n</sub> )	expressint <sub>0</sub> expressint <sub>n</sub>
scope resolution	~symbol	(resolve-scope symbol)
bit extraction	ever[int]	(resolve-group sympol)
slice extraction	evpr[int.int.]	(slice ever int. int.)
expression quoting	üever	(mote ever)

